

# XILINX JTAG DIGILENT USB

## 特性：

- > 适用Xilinx的多功能JTAG\SPI编程器
- > JTAG / SPI频率可由用户配置
- > 下载最高速度高达30Mbit/sec
- > 兼容所有Xilinx工具特别是Vivado,iMPACT, Chipscope和EDK,SDK (ISE 13.2+,Vivado 2012.1+)
- > 自动适应目标信号线电平范围：1.8-5V
- > 使用SPI编程解决方案（支持模式0和2）
- > 完全支持Adept SDK，允许自定义JTAG / SPI应用
- > 支持Windows XP / 7 / 8 / 10以及Linux操作系统
- > 双色LED指示状态
- > 驱动直接包含在xilinx软件中，选择安装即可。（也可单独提供）

1. All Xilinx FPGAs。
2. Xilinx Zynq-7000
3. Xilinx CoolRunner™/CoolRunner-II CPLDs
4. Xilinx Platform Flash ISP configuration PROMs
5. Select third-party SPI PROMs
6. Select third-party BPI PROMs

## Support ALL FPGA Device (future new devices)

☆Spartan-7Spartan-6Artix-7Zynq-7000

☆Kintex-7Virtex-7Kintex UltraScale

☆Virtex UltraScale Kintex UltraScale+

☆Virtex UltraScale+

☆Virtex-5 FX, Virtex-5 LX, Virtex-5 SX

☆Virtex-4, Virtex-II Pro

☆Virtex-II, Virtex-E

☆Virtex, Spartan-5

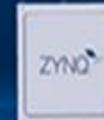
☆Spartan-3A, Spartan-3A DSP

☆Spartan-3AN, Spartan-3E

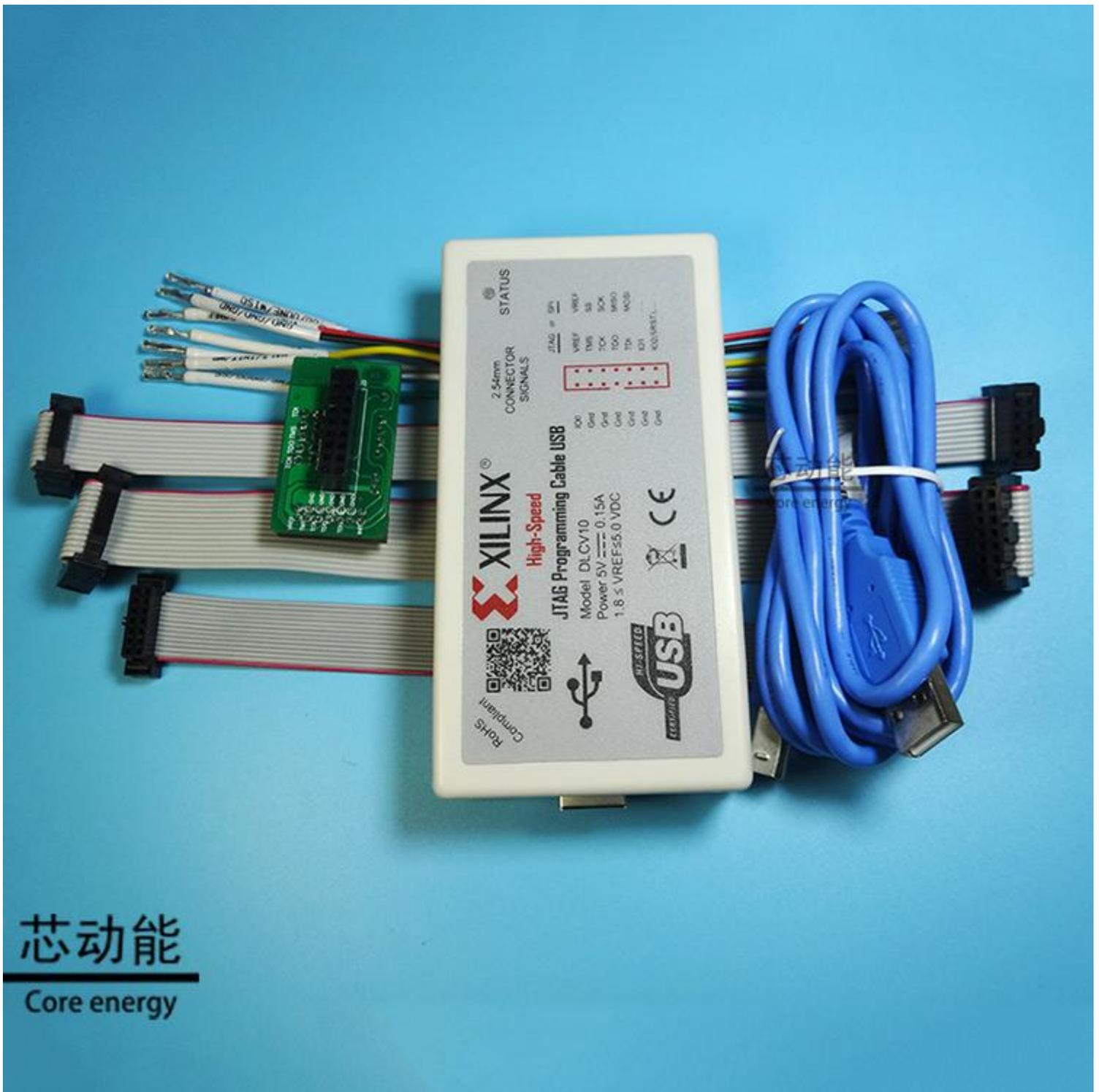
☆Spartan-3, Spartan-II

☆Spartan-IIE, Spartan

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1. Ordered List Item Xilinx 9500/9500XL CPLDs
2. Xilinx 1700 and 18V00 ISP configuration PROMs
3. Xilinx FPGA eFUSE programming

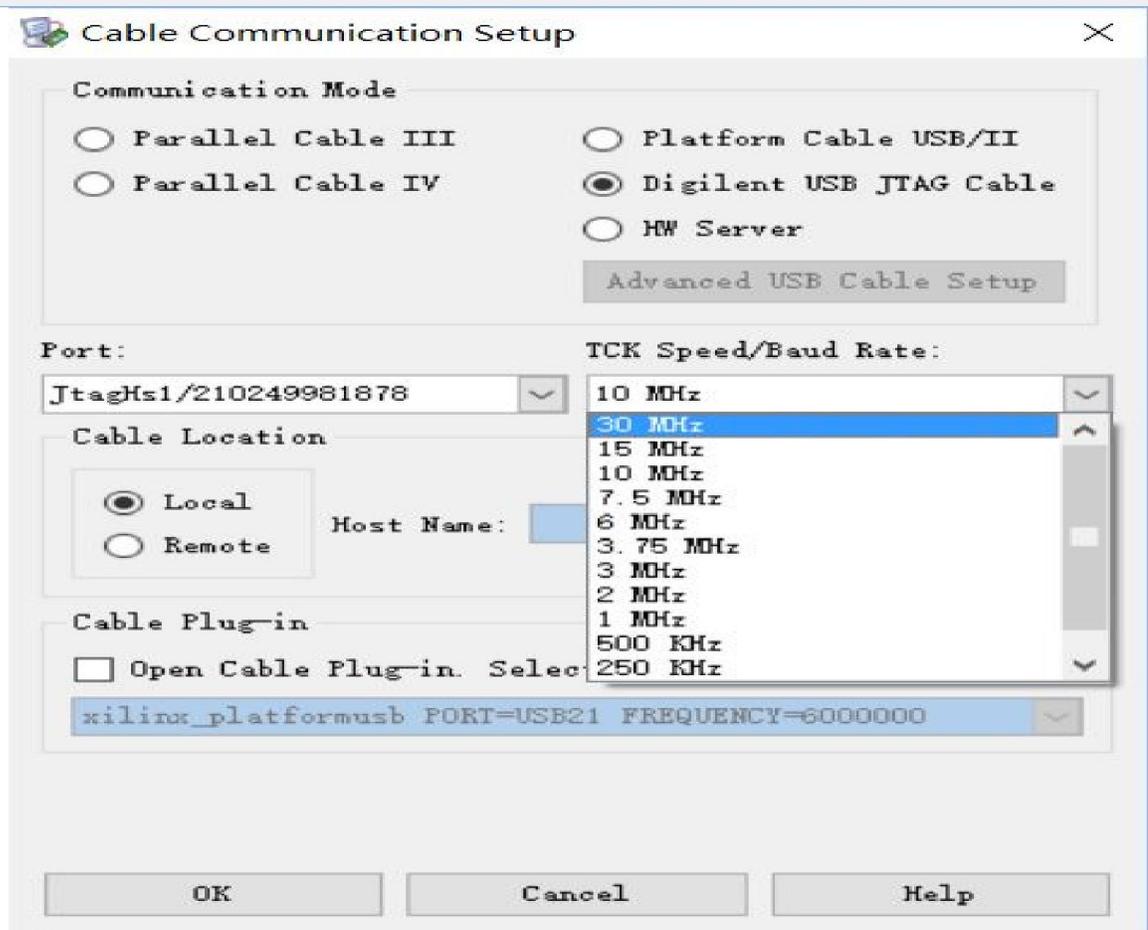
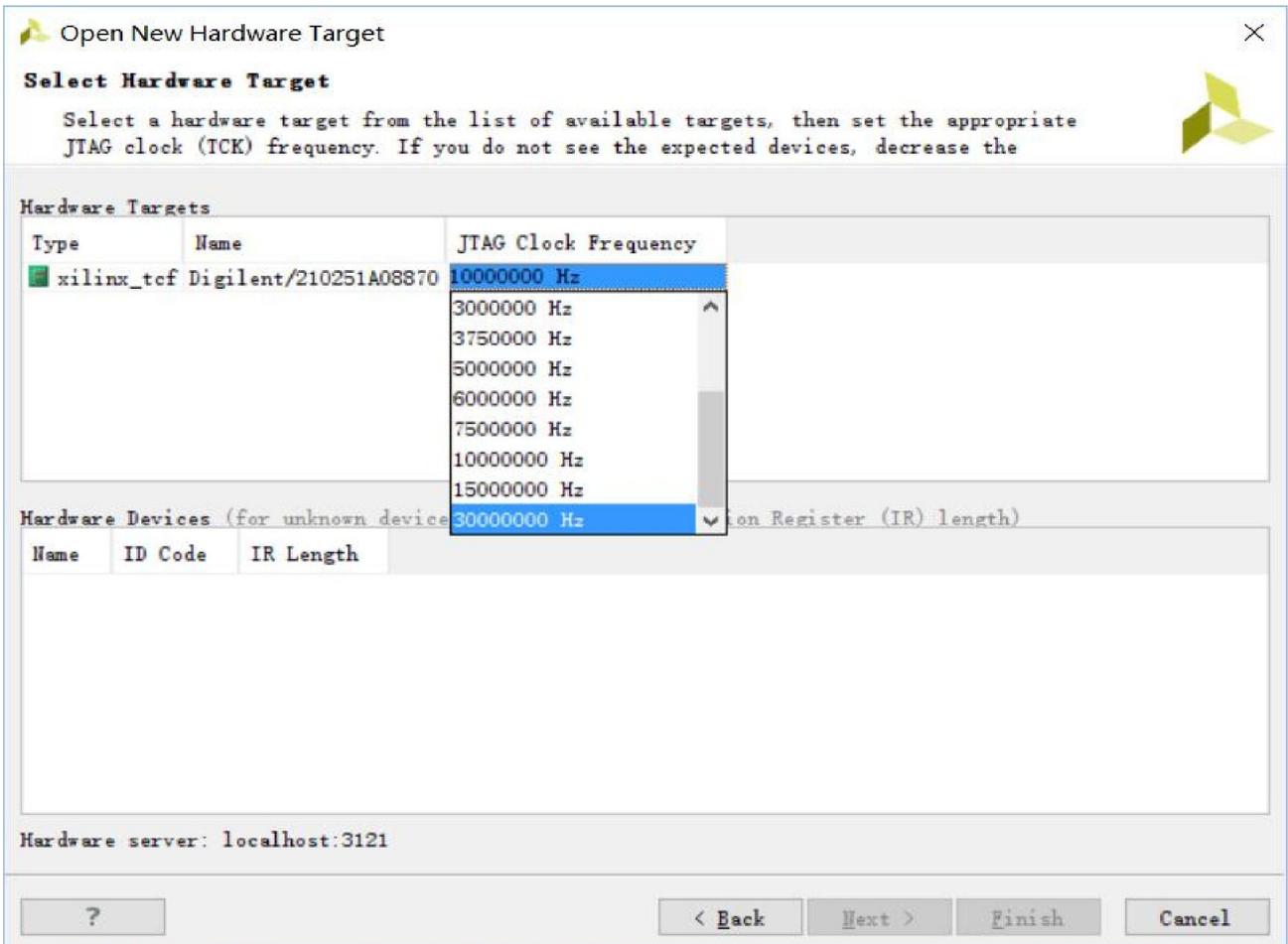


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**The actual maximum download speed is determined for the following reasons:**

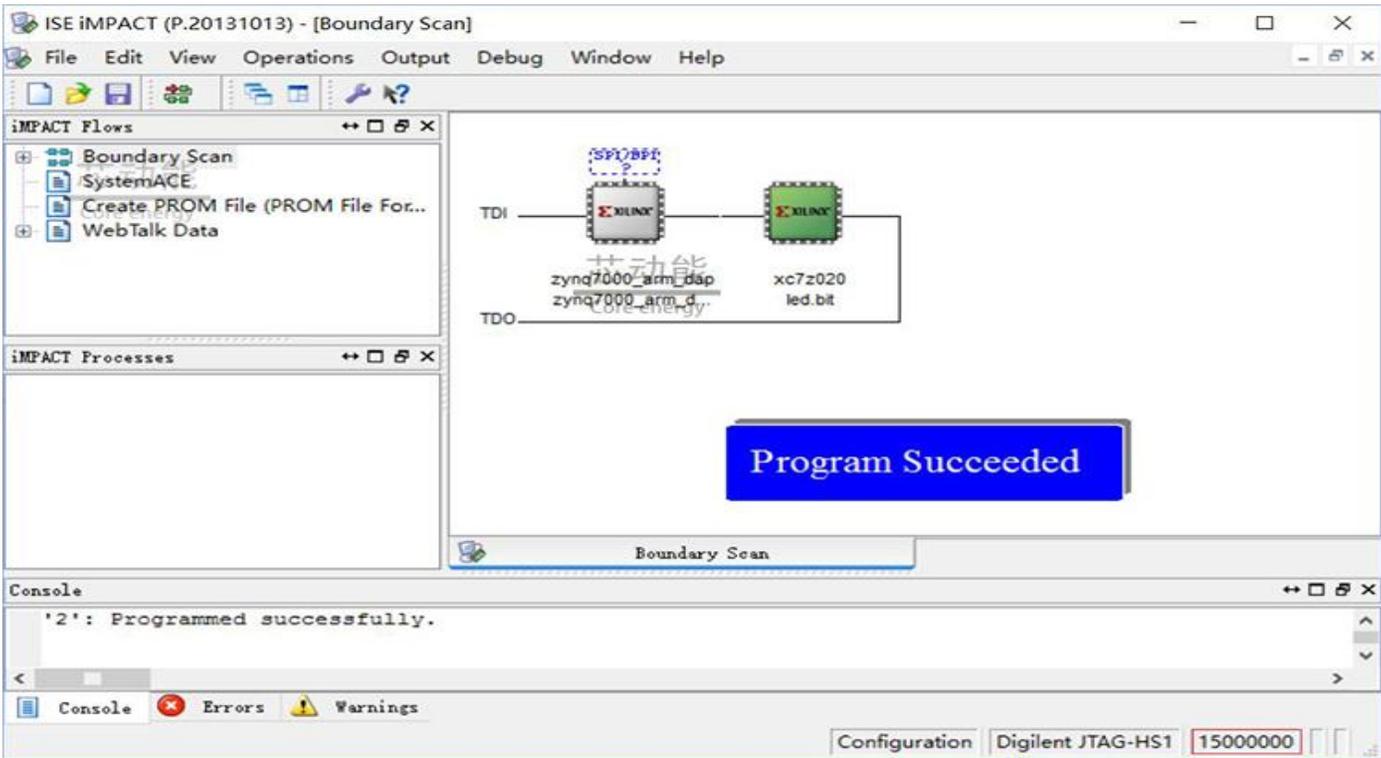
- (1) Set the required download speed in the xilinx software: the default configuration of the software is generally 10MHz; you can try to increase it. When the speed is not applicable to the target device, please slow down the software; as shown below:



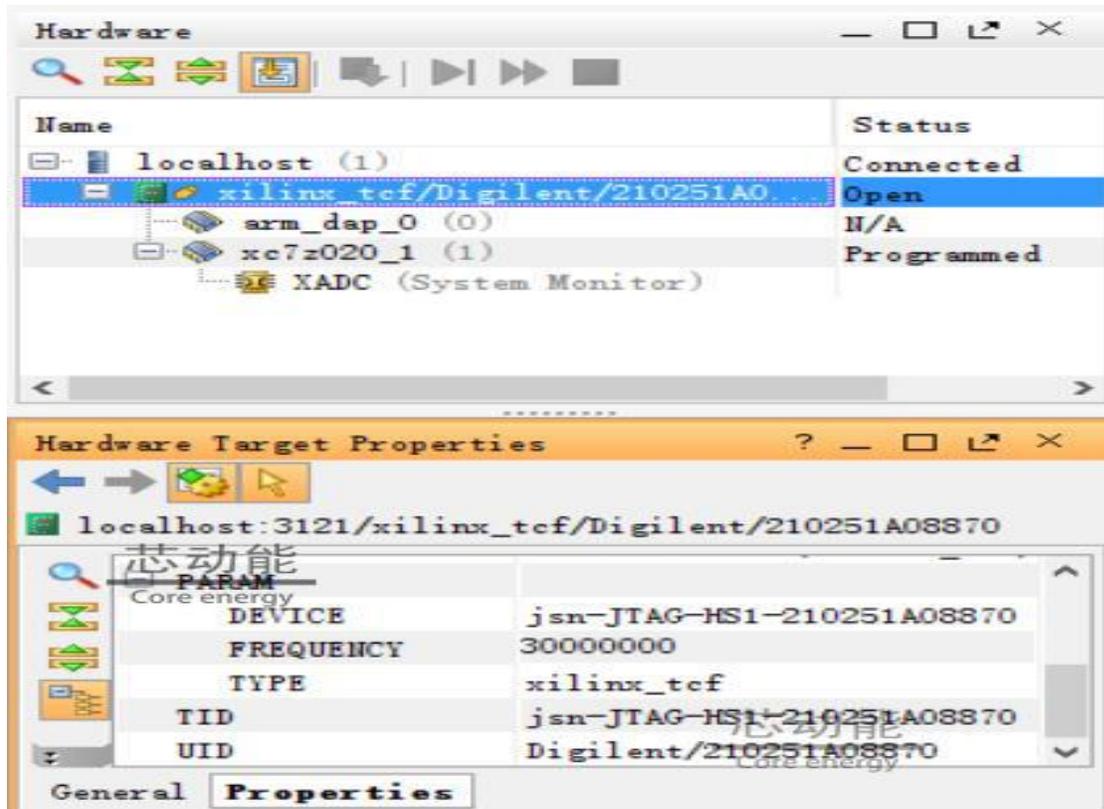
(2) The signal integrity on the link of the target device itself needs to support the selected rate;

Speed measured results:

**impact: 15MHZ**



**Vivado: 30MHZ**

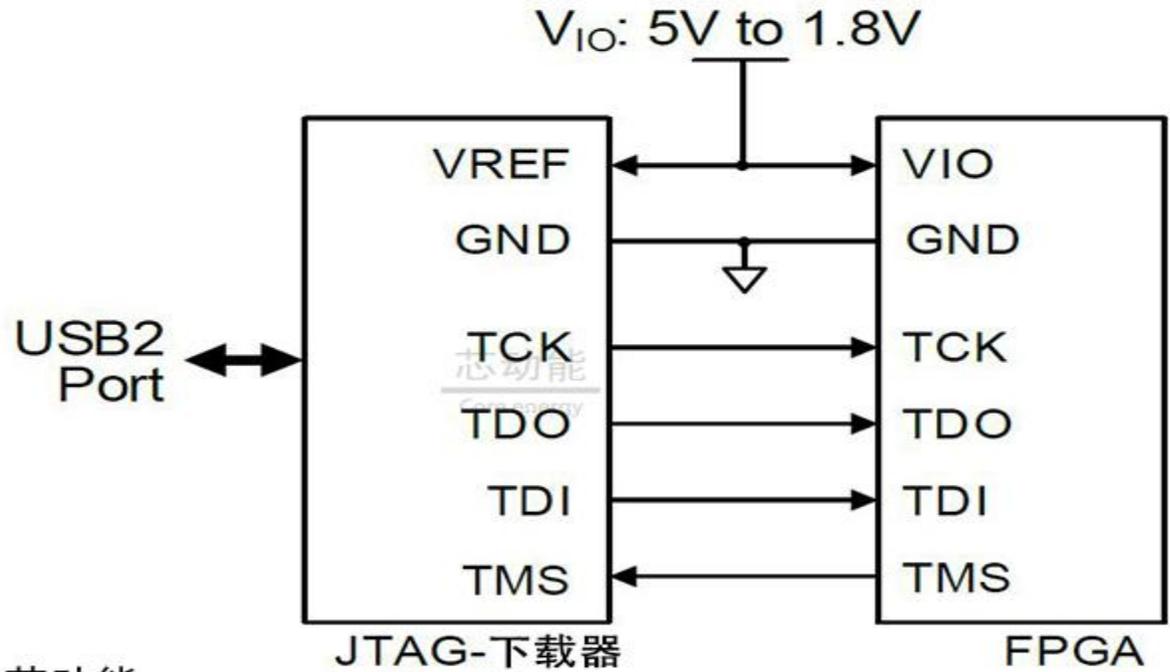


**Selection guide:**

选择指南 (Selection Guide)	MT2	HS3	HS2	HS1
最大速度 (Max Speed)	30 Mhz	30 Mhz	30 Mhz	30 Mhz
电压范围 (Voltage Range)	1.8 - 5 V			
Xilinx原生支持 (Xilinx Native Support)	ISE 14.1+ Vivado 2013.1+	ISE 14.1+ Vivado 2013.1+	ISE 13.2+ Vivado 2012.1+	ISE 13.2+ Vivado 2012.1+
Xilinx插件支持 (Xilinx Plug-in Support)	ISE 13.1+	ISE 12.1+	ISE 12.1+	ISE 12.1+
电脑接口 (PC Interface)	USB	USB	USB	USB
Digilent Adept支持 (Digilent Adept Support)	OK	OK	OK	OK
4线JTAG (4-Wire JTAG)	OK	OK	OK	OK
2线JTAG (2-Wire JTAG)	OK	NO	OK	NO
ZYNQ-7000 PS_SRST Support (ZYNQ-7000 PS_SRST支持)	OK	OK	NO	NO
SPI支持 (SPI Support)	OK	NO	OK	OK

The Xilinx JTAG Downloader is connected to the target board:

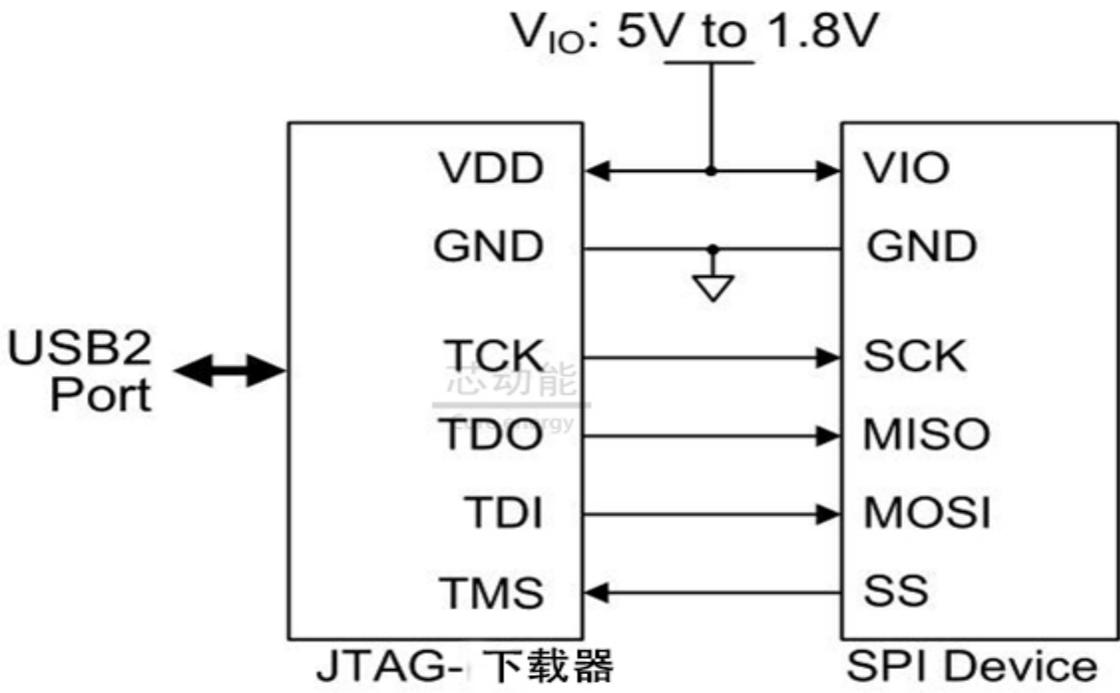
1. JTAG connection.



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Diagram of signal voltages and connections.

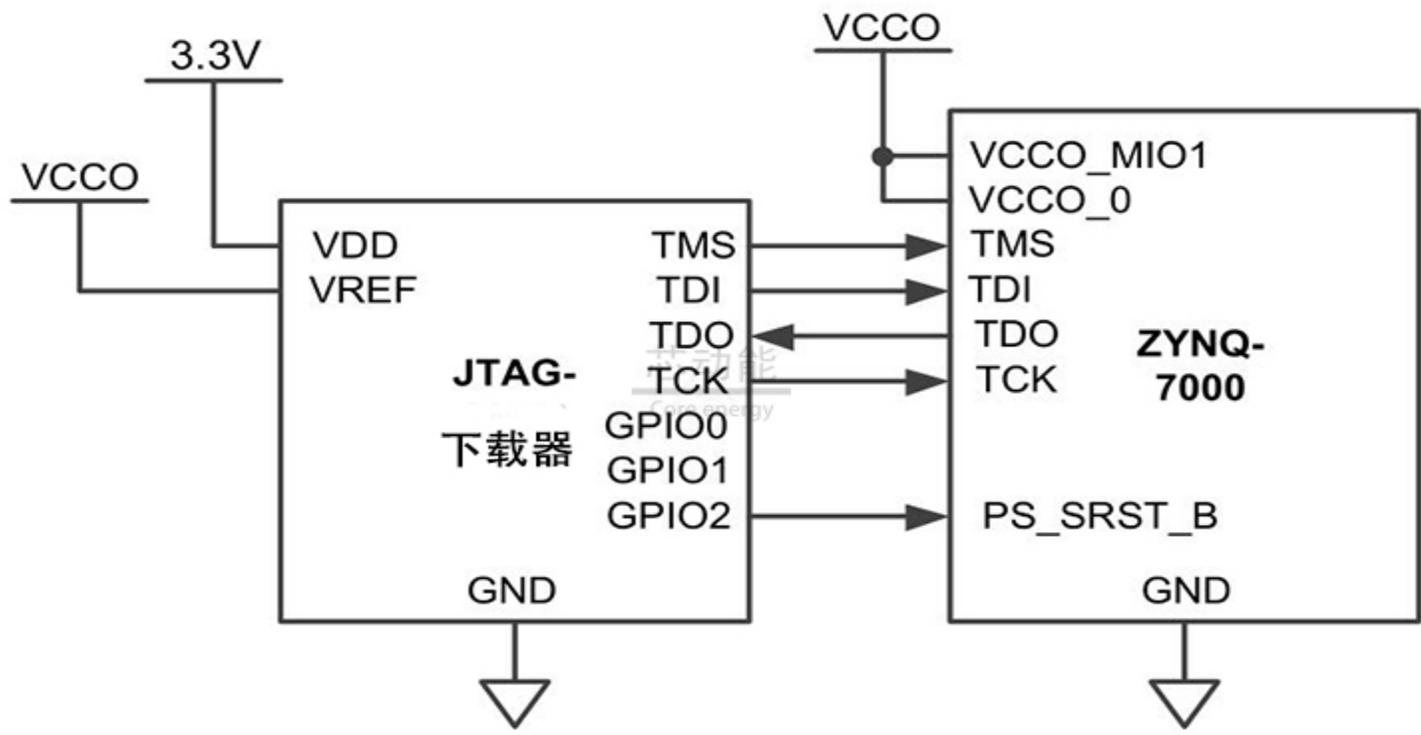
2. SPI connection.



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SPI Device Connections

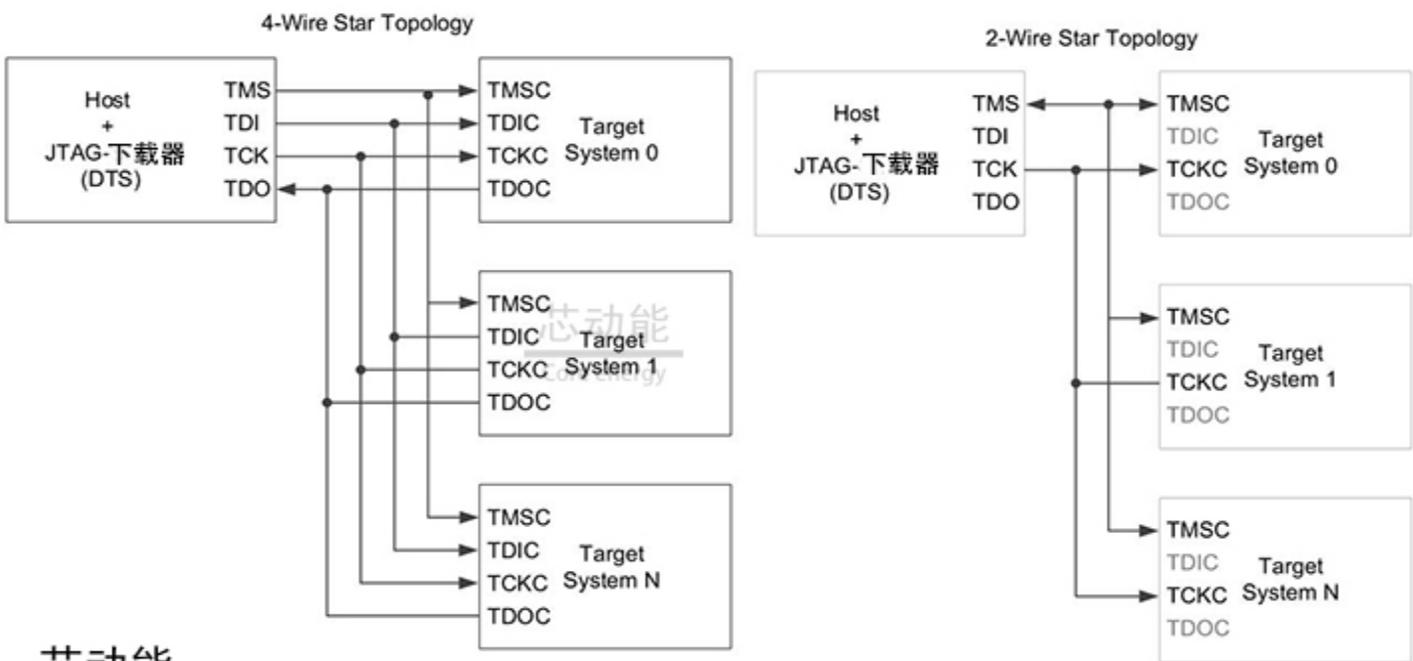
3. Xilinx's Zynq-7000 PS\_SRST\_B connection.



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Connecting the JTAG-下载器MT2 to Xilinx's Zynq-7000.

4-Wire and 2-Wire star topology.



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4-Wire and 2-Wire star topology.

**Shipping list:**

Standard version

Xilinx JTAG Downloader 1 piece

USB2.0 line 1 piece

2.54mm 14P cable 1 piece

**Set version :**

Xilinx JTAG Downloader 1 piece

USB2.0 line 1 piece

Adapter plate 1 piece

2.0mm 14P cable 1 piece

2.54mm 10P cable 1 piece

Color cable 1 piece