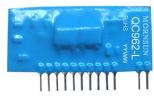
MORNSUN®

QC962-L Hybrid Integrated IGBT Driver

QC962-L is a hybrid integrated IGBT driver designed for driving N-channel IGBT modules in any gate amplifier application. The device provides the required electrical isolation between input and output with the opto-coupler. Short circuit protection is provided by a built-in desaturation detector. A fault signal is provided if the short circuit protection is activate.



RoHS

FEATURES

- Built in high CMRR opto-coupler (CMR:Typical: 30kV/µs, Min.:15kV/µs)
- Two supply drive topology
- TTL compatible input interface
- Electrical isolation voltage between input and output with opto-couplers (Viso=3750VRMS/min)
- Built in short circuit protection circuit with a pin for fault output
- Controlled time detected short circuit
- Switching frequency up to 20kHz
- Pin and characteristic are compatible with M57962AL

APPLICATION

- General-purpose Inverter
- AC Servo Systems
- Uninterruptable Power Supplies(UPS)
- Welding Machines

RECOMMENDED MODULES

- 600V Series IGBT(up to 600A)
- 1200V Series IGBT(up to 400A)
- 1700V Series IGBT(up to 200A)

MORNSUN	Science	&	Technology	Co.,Ltd.

Address: No. 5, Kehui St. 1, Kehui development center, Science Ave., Guangzhou Science City, Luogang district, Guangzhou,P.R.China. Tel: 86-20-38601850 Fax:86-20-38601272 <u>Http://www.mornsun-power.com</u>

ABSOLUTE MAXIMUM RATINGS				
Item		Test Conditions	Limit	Units
Supply Voltage*		DC	18	V
Supply voltage	V _{EE}		-15	V
Input Current	lin	Between pin13 and pin14 25		mA
Output Voltage	Vo	Output voltage "H"	V _{cc}	V
Output Current		Pulse width 2µs	+5	А
		Frequency f=20kHz	-5	Α
Isolation Voltage	Viso	Sine wave voltage 50Hz / 60Hz,1 min.	3750	V
Junction Temperature	TJ		150	°C
Operation Temperature	T _{op}		-20~+70	°C
Storage Temperature	T _{st}	-40~+125		°C
Fault Output Current	IFO	Pin8 input current 20		mA
Input Voltage	V _{R1}	Pin 1 voltage 50		V

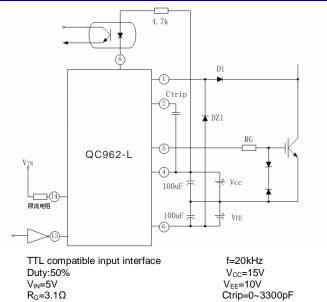
2. *20V<V_{CC}-V_{EE}<28V.

ELECTRICAL CHARACTERISTIC

Characteristics		Test Conditions	Value			1.1
		Test Conditions	Min	Тур.	Max	Units
Supply Voltage	V _{cc}	Recommended Range	14	15		V
	V _{EE}	Recommended Range	-7		-10	V
Switching frequency	f	Recommended Range	0		20	kHz
Gate resistant	R _G	Recommended Range	2			Ω
"H" input current	IIH	Recommended Range	10	16	20	mA
"H" output voltage	V _{OH}		13	14		V
"L" output voltage	Vol		-6		-9	V
"L-H" propagation	t _{PLH}	I _{IH} =16mA		0.5	1	μs
"L-H" rise time	tr	I _{IH} =16mA		0.6	1	μs
"H-L" propagation	t _{PHL}	I _{IH} =16mA		1	1.3	μs
"H-L" fall time	t _f	I _{IH} =16mA		0.4	1	μs
Protection reset time	t _{timer}		1	1.3	2	ms
Fault output current	I _{FO}	Pin8 input current, R=4.7K		5		mA
Controlled time detect short circuit 1	T _{trip1}	Pin1: ≥15V,Pin2:open		2.6		μs
Controlled time detect short circuit 2	T _{trip2}	Pin1:≥15V Pin2- Pin4:1000pF		3		μs
Soft turn-off time	T _{off2}	PIN1:≥15V		5		μs
SC detect voltage	Vsc	Collector voltage of module	15			V

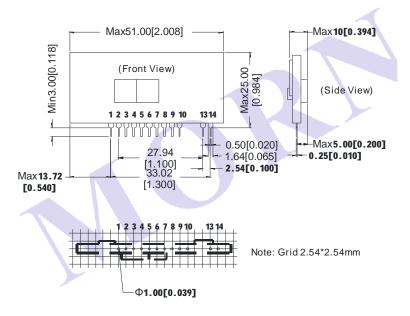
Notes: 1. Ta=25°C, V_{CC}=15V, V_{EE}=-10V. unless otherwise specified 2."H"represents high level; "L" represents low level.

APPLICATION EXAMPLES



D₁ :Fast Recovery Diode(trr≤0.2µs)

OUTLINE DRAWING



Note: Unit: mm[inch] Pin section tolerances : ± 0.10mm[± 0.004inch] General tolerances : ± 0.50mm[± 0.020inch]

APPLICATION NOTES

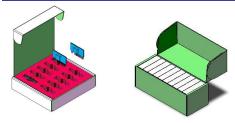
- The IGBT gate-emitter drive loop wiring must be shorter 1 than one meter.
- 2. The IGBT gate-emitter drive loop wiring should be twisted.
- 3. If large voltage spike is generated at the collector of the IGBT, increase the IGBT gate resistor.
- 4. Pin3,7,9,10 are used only for the test circuit and not be connected with the application circuit.
- 5. The external blocking capacitors must be connected as close as possible to the driver's pin.
- 6. Peak reverse voltage of the diode D1 must be higher than the peak value of the IGBT collector voltage
- 7. The distance between the capacitor Ctrip and pin2-4 should be as short as possible(Max.5cm)
- 8. Pin1 voltage could be high due to the reverse recovery characteristic of the diode D1 and the 30V zener diode DZ1 is connected between pin1 and pin6 for protecting the driver.
- The input signal voltage must be less than 5.25V. The 9. higher input signal voltage, the higher input signal current. It will result in more dissipation. The input port is a circuit composed of a high-speed optocoupler series with a 150ohm resistor. Practically, a current-limiting resistor is inserted, which value can be obtained according to the following equation:

$$R = \frac{Vin - 1.7V}{16mA} - 150ohm$$

PIN FUNCTION

Pin number	Description	
1	Fault detect	
2	Reaction time	
4	Power supply(+)	
5	Drive output	
6	Power supply(-)	
8	Fault signal output	
13	Drive signal input(-)	
14	Drive signal input(+)	
3, 7, 9, 10	Not connected	

PACKAGE DIAGRAM



(small white box) Small white box dimensions:L*W*H=163*150*35mm Packaging quantity: 10PCS Inner packaging box dimensions:

L*W*H=430*175*160mm

Packaging quantity: 100PCS Outer packaging carton dimensions

L*W*H=560*450*520mm

Packaging quantity: 900PCS

(inner packaging box)