4560BBCD ADDER



 $\label{eq:decomposition} \mbox{DESCRIPTION} - \mbox{The } 4560B \mbox{ is a BCD Adder with two } 4\mbox{-bit Data Inputs } (\mbox{A}_0\mbox{-}\mbox{A}_3, \mbox{ B}_0\mbox{-}\mbox{B}_3), \mbox{ a Carry Input } (\mbox{C}_0), \mbox{ four Sum Outputs } (\mbox{S}_0\mbox{-}\mbox{S}_3) \mbox{ and a Carry Output } (\mbox{C}_4).$

The 4560B uses full lookahead across 4-bits to generate the Carry Output (C₄). This minimizes the necessity for extensive "lookahead" and carry-cascading circuits.

In connection with the 4561B, 9's Complementer, a configuration for subtracting two BCD numbers can also be attained.

• FULLY CARRY LOOKAHEAD ACROSS FOUR BITS

EASILY CASCADED

PIN NAMES	FUNCTION
A ₀ , B ₀ , A ₁ , B ₁	Data Inputs
A_2 , B_2 , A_3 , B_3	Data Inputs
c ₀	Carry Input
s ₀ -s ₃	Sum Outputs
C ₄	Carry Output



