

63

UNRES

Only

006074

041811

Revision 1

6074

NEC

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Description

The μPD27C64 is a 65,536-bit (8,192 × 8-bit) electrically programmable read-only memory (EPROM). It operates from a single +5V power supply making it ideal for microprocessor applications. It is fabricated using an advanced CMOS process which saves substantial power in operating and standby modes.

A distinctive feature of the μPD27C64 is an output enable (OE) separate from the chip enable (CE). The OE control eliminates bus contention in multiple-bus microprocessor systems. The μPD27C64 features fast, simple, one-pulse programming controlled by TTL-level signals. A high-speed programming mode is also available.

The μPD27C64 is available in a cerdip package with a quartz window as an ultraviolet (UV), erasable EPROM, or in a plastic package as a one-time-programmable (OTP), non-erasable EPROM.

Features

- Ultraviolet erasable and electrically programmable
- Low supply current:
 - 30 mA (max) active current
 - 100 μA (max) standby current
- High-speed programming mode
- Single location programming
- Programmable with single pulse (total programming time is 420 sec in standard mode)
- Input/output TTL-compatible
- Single +5 V power supply
- Low power dissipation:
 - 33 mW/MHz (max) operating
 - 550 μW (max) standby
- μPD2764-compatible
- 28-pin DIP

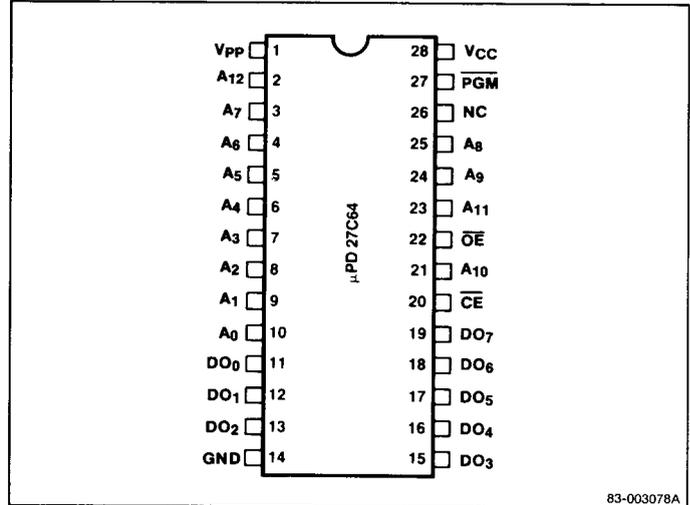
Performance Ranges

Device	Access Time (Max)	Power Supply (Max)	
		Active	Standby
μPD27C64-20	200 ns	30 mA	100 μA
μPD27C64-25(1)	250 ns	30 mA	100 μA
μPD27C64-30(1)	300 ns	30 mA	100 μA

Note:

(1) Available as either UV or OTP.

Pin Configuration

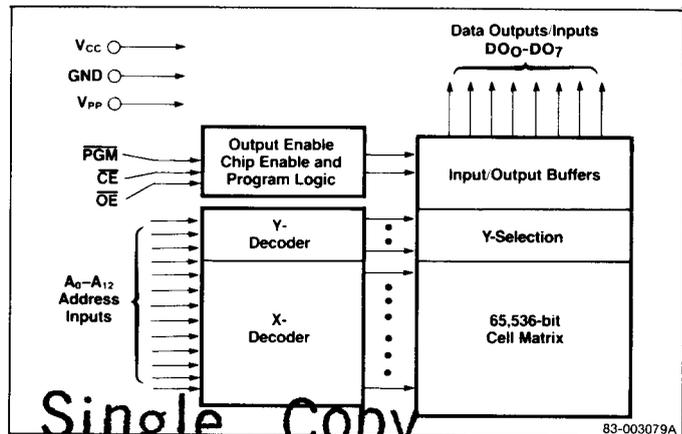


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Pin Identification

No.	Symbol	Function
1	V _{PP}	Program voltage
2-10, 21, 23-25	A ₀ -A ₁₂	Address inputs
11-13, 15-19	DO ₀ -DO ₇	Data inputs / outputs
14	GND	Ground
20	CE	Chip enable
22	OE	Output enable
26	NC	No connection
27	PGM	Program
28	V _{CC}	+5 V power supply

Block Diagram



83-003079A

Single Copy

Handle With Care

