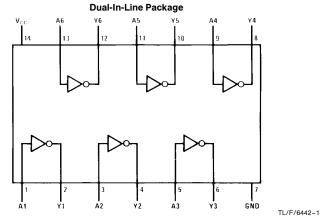


# DM54S04/DM74S04 Hex Inverting Gates

# **General Description**

This device contains six independent gates each of which performs the logic INVERT function.

## **Connection Diagram**



Order Number DM54S04J, DM54S04W, DM74S04M or DM74S04N See NS Package Number J14A, M14A, N14A or W14B

### **Function Table**

$\mathbf{Y} = \overline{\mathbf{A}}$					
Input	Output				
Α	Υ				
L	Н				
l н	L				

$$\begin{split} H &= \text{High Logic Level} \\ L &= \text{Low Logic Level} \end{split}$$

#### **Absolute Maximum Ratings (Note)**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 5.5V
Operating Free Air Temperature Range

DM54S -55°C to +125°C DM74S 0°C to +70°C

Storage Temperature Range  $-65^{\circ}\text{C} \text{ to } +150^{\circ}\text{C}$ 

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

# **Recommended Operating Conditions**

Symbol	Parameter	DM54S04			DM74S04			Units
		Min	Nom	Max	Min	Nom	Max	Omis
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V <sub>IH</sub>	High Level Input Voltage	2			2			V
V <sub>IL</sub>	Low Level Input Voltage			0.8			0.8	V
	High Level Output Current			-1			-1	mA
loL	Low Level Output Current			20			20	mA
T <sub>A</sub>	Free Air Operating Temperature	-55		125	0		70	°C

#### Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

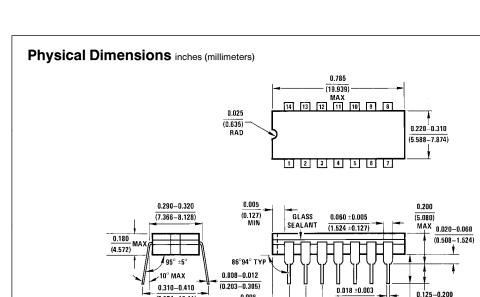
Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$				-1.2	V
V <sub>OH</sub>	V <sub>OH</sub> High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$	DM54	2.5	3.4		V
		V <sub>IL</sub> = Max	DM74	2.7	3.4		
V <sub>OL</sub>	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min$				0.5	V
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
I <sub>IH</sub>	High Level Input Current	$V_{CC} = Max, V_I = 2.7V$				50	μΑ
I <sub>IL</sub>	Low Level Input Current	$V_{CC} = Max, V_I = 0.5V$				-2	mA
los	I <sub>OS</sub> Short Circuit	V <sub>CC</sub> = Max	DM54	-40		-100	- mA
Output Cu	Output Current	(Note 2)	DM74	-40		-100	
Іссн	Supply Current with Outputs High	V <sub>CC</sub> = Max			15	24	mA
ICCL	Supply Current with Outputs Low	V <sub>CC</sub> = Max			30	54	mA

#### Switching Characteristics at V<sub>CC</sub> = 5V and T<sub>A</sub> = 25°C (See Section 1 for Test Waveforms and Output Load)

	Parameter					
Symbol		C <sub>L</sub> =	15 pF	$C_L = 50  pF$		Units
		Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay Time Low to High Level Output	2	4.5	2	7	ns
t <sub>PHL</sub>	Propagation Delay Time High to Low Level Output	2	5	2	8	ns

Note 1: All typicals are at  $V_{CC}=5V,\,T_A=25^{\circ}C.$ 

Note 2: Not more than one output should be shorted at a time, and the duration should not exceed one second.



0.098

(2.489)

MAX BOTH ENDS

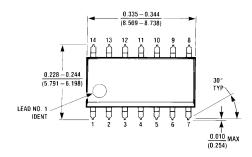
(7.874-10.41)

14-Lead Ceramic Dual-In-Line Package (J) Order Number DM54S04J **NS Package Number J14A** 

(0.457 ±0.076)

0.100 ±0.010

(2.540 ±0.254)

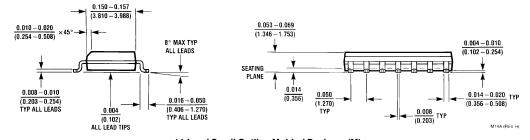


(3.175-5.080)

J14A (REV G)

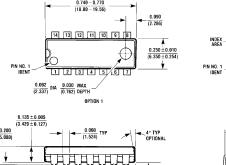
0.150

(3.81)



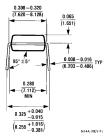
14-Lead Small Outline Molded Package (M) Order Number DM74S04M NS Package Number M14A

# Physical Dimensions inches (millimeters) (Continued)

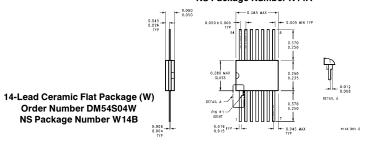




OPTION 02



#### 14-Lead Molded Dual-In-Line Package (N) Order Number DM74S04N NS Package Number N14A



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