TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4051BP, TC4051BF, TC4051BFT TC4052BP, TC4052BF, TC4052BFT TC4053BP, TC4053BF, TC4053BFT

TC4051B

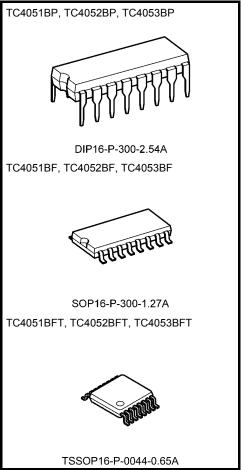
Single 8-Channel Multiplexer/Demultiplexer TC4052B

Differential 4-Channel Multiplexer/Demultiplexer

TC4053B

Triple 2-Channel Multiplexer/Demultiplexer

TC4051B, TC4052B and TC4053B are multiplexers with capabilities of selection and mixture of analog signal and digital signal. TC4051B has 8 channels configuration. TC4052B has 4 channel  $\times$  2 configuration and TC4053B has 2 channel  $\times$  3 configuration. The digital signal to the control terminal turns "ON" the corresponding switch of each channel, with large amplitude (VDD – VEE) can be switched by the control signal with small logical amplitude (VDD – VSS). For example, in the case of VDD = 5 V VSS = 0 V and VEE = –5 V, signals between –5 V and +5 V can be switched from the logical circuit with single power supply of 5 volts. As the ON-resistance of each switch is low, these can be connected to the circuits with low input impedance.

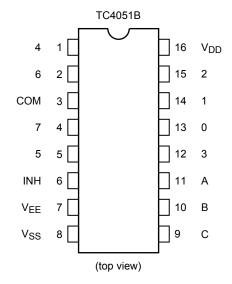


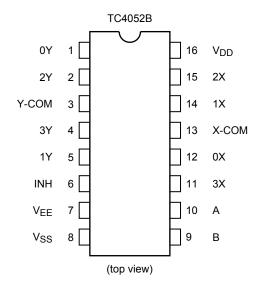
Weight

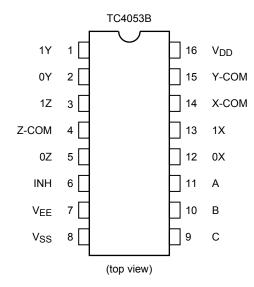
DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) TSSOP16-P-0044-0.65A : 0.06 g (typ.)



## **Pin Assignment**







#### **Truth Table**

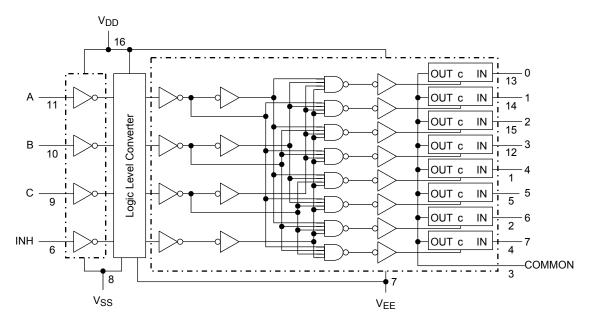
	Contro	Inputs	-	"ON" Channel					
Inhibit	Inhibit C $\Delta$			TC4051B	TC4052B	TC4053B			
L	L	L	L	0	0X, 0Y	0X, 0Y, 0Z			
L	L	L	Н	1	1X, 1Y	1X, 0Y, 0Z			
L	L	Н	L	2	2X, 2Y	0X, 1Y, 0Z			
L	L	Н	Н	3	3X, 3Y	1X, 1Y, 0Z			
LH		L	L	4	_	0X, 0Y, 1Z			
LH		LH		5	— 1X,	0Y, 1Z			
LH		ΗL		6	— 0X,	1Y, 1Z			
LH		Н	Н	7	— 1X,	1Y, 1Z			
Н	Х	Х	Х	None	None	None			

X: Don't care

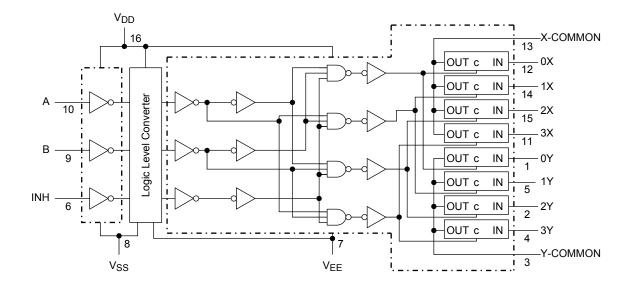
Δ: Except TC4052B

# **Logic Diagram**

## TC4051B

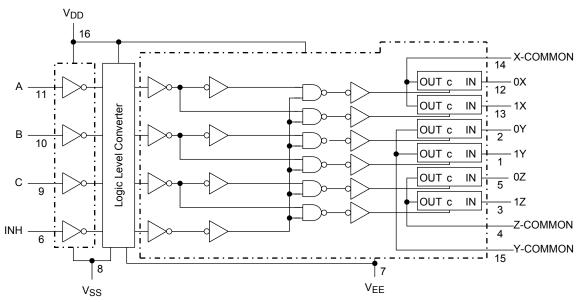


### TC4052B



3

#### TC4053B



#### **Truth Table**

Control			
С	IN-O	UT	(Note)
Н		0.5 to 5 $\times$ 10 $^2\Omega$	
L		$>$ 10 $^{9}$ $\Omega$	

Note: See electrical characteristics

## **Absolute Maximum Ratings (Note)**

Characteristics S	ymbol	Rating	Unit
DC supply voltage	$V_{DD}$ - $V_{SS}$	−0.5 to 20	V
DC supply voltage	V <sub>DD</sub> -V <sub>EE</sub>	-0.5 to 20	V
Control input voltage	V <sub>CIN</sub>	$V_{SS}-0.5$ to $V_{DD}+0.5$	V
Switch I/O voltage	V <sub>I</sub> /V <sub>O</sub>	V <sub>EE</sub> – 0.5 to V <sub>DD</sub> + 0.5	V
Control input current	I <sub>CIN</sub>	±10	mA
Potential difference across I/O during ON	V <sub>I</sub> -V <sub>O</sub>	-0.5 to 0.5	V
Power dissipation	$P_{D}$	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	−40 to 85	°C
Storage temperature range	T <sub>stg</sub>	-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



# **Operating Ranges (Note)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
DC supply voltage	$V_{DD}$ - $V_{SS}$	_	3	_	18	V	
Do supply voltage	$V_{DD}$ - $V_{EE}$	—3		— 18			
Control input voltage	V <sub>IN</sub>	_	V <sub>SS</sub>	_	$V_{DD}$	٧	
Input/output voltage	V <sub>IN</sub> /V <sub>OUT</sub>	_	V <sub>EE</sub>	_	$V_{DD}$	V	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused Control inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

## **Static Electrical Characteristics**

		Test Condition				−40°C 25°C					85°C		1.1
Characteristics S	ymbol		V <sub>SS</sub> (V)	V <sub>EE</sub>	V <sub>DD</sub> (V)	Min	Max	Min	Тур. Л	lax	Min	Max	Unit
			\/	V	5	3.5	_	3.5	2.75	_	3.5	_	
Control input high voltage	$V_{IH}$		$V_{EE} = V_{SS}$ $R_L = 1 \text{ k}\Omega$		10	7.0	_	7.0	5.50	_	7.0	_	V
		$V_{IS} = V_{DD}$	to V <sub>S</sub>	S S	15	11.0	_	11.0	8.25	_	11.0	_	
Combrel in next lave		thru 1 kΩ	IIS <	2 μΑ	5	_	1.5	_	2.25	1.5	_	1.5	
Control input low voltage	$V_{IL}$		on all		10	_	3.0	_	4.5	3.0	_	3.0	V
				1	15		4.0	_	6.75	4.0	_	4.0	
On state		$0 \le V_{IS} \le V_{DD}$	0	0	5	_	850	_	240	950	_	1200	
On-state resistance	R <sub>ON</sub>	$R_L = 10 \text{ k}\Omega$	0	0	10	_	210	_	110	250	_	300	Ω
			0	0	15	_	140	_	80	160	_	200	
ΔOn-state	R <sub>ON</sub> ∆		0	0	5	_	_	_	10	_	_	_	
resistance between any 2		_	0	0	10	_	_	_	6	_	_	_	Ω
switches			0	0	15	_	_	_	4	_	_	_	
Input/output	loff	$V_{IN} = 18 \text{ V}, V_{OUT} = 0 \text{ V}$ $V_{IN} = 0 \text{ V}, V_{OUT} = 18 \text{ V}$			18	_	±100	_	±0.01	±100	_	±1000	nA
leakage current					18	_	±100	_	±0.01	±100	_	±1000	
	I <sub>DD</sub>	$V_{IN} = V_{SS}, V_{DD}$ (Note)		5	_	5.0	_	0.005	5.0	_	150		
Quiescent supply current				10	_	10	_	0.010	10	_	300	μА	
					15	_	20	_	0.015	20	_	600	
Input current	I <sub>IN</sub>	V <sub>IH</sub> = 18 V			18	_	0.1	_	10 <sup>-5</sup>	0.1	_	1.0	μΑ
input current		V <sub>IL</sub> = 0 V			18	_	-0.1	_	-10 <sup>-5</sup>	-0.1	_	-1.0	μΛ
Input capacitance	C <sub>IN</sub>	_			_	_	_	—5		7.5	_	_	pF
Switch input capacitance	$C_{IN}$	_				_	_	—10		_		_	pF
	TC4051B				10	_	_	_	58	_	_	_	
Output capacitance	C <sub>OUT</sub>	TC4052B		10	_	_	_	30	_	_	_	pF	
		TC4053B		10	_	_	_	17	_	_	_		
		TC4051B		-	10	_	_	_	0.2	_	_	_	
Feedthrough capacitance	C <sub>IN</sub> - C <sub>-OUT</sub>	TC4052B			10	_	_	_	0.2	_	_	_	pF
	0-001	TC4053B			10			_	0.2	_			

Note: All valid input combinations.



# Dynamic Electrical Characteristics (Ta = 25°C, C<sub>L</sub> = 50 pF)

		Te								
Characteristics S	ymbol		V <sub>SS</sub> (V)	V <sub>EE</sub> (V)	V <sub>DD</sub> (V)	Min T	yp.	Max	Unit	
		_		0	0	5	_	15	45	
Phase difference between input to output	φι-О			0	0	10	_	8	20	ns
			0	0	15	_	6	15		
	+			0	0	5	_	170	550	
Propagation delay time	t <sub>p</sub> ZL			0	0	10	_	90	240	
(A, B, C, -OUT)	t <sub>pZH</sub>	$R_L = 1 \text{ k}\Omega$		0	0	15	_	70	160	ns
(A, B, C, -001)	t <sub>pLZ</sub> t <sub>pHZ</sub>			0	-5	5	_	100	240	
	фни			0	-7.5	7.5	_	80	160	
				0	0	5	_	120	380	
Propagation delay time	t71			0	0	10	_	60	200	ns
(INH-OUT)	t <sub>P</sub> ZL t <sub>P</sub> ZH	$R_L = 1 \text{ k}\Omega$		0	0	15	_	50	160	
(1111 001)				0	-5	5	_	80	200	
				0	-7.5	7.5	_	60	160	
	t <sub>pLZ</sub>			0	0	5	_	170	450	
Propagation delay time		$R_L = 1 \text{ k}\Omega$		0	0	10	_	90	210	
(INH-OUT)				0	0	15	_	70	160	ns
(	φηΖ			0	-5	5	_	100	210	
				0	-7.5	7.5	_	80	160	
-3dB cutoff frequency				<b>–</b> 5	<b>–</b> 5	5	_	20	_	
TC4051B	f <sub>max</sub> (I-O)	$R_L = 1 \text{ k}\Omega$ (Note	1)		_5	5	_	30	_	MHz
TC4052B	······································		-,	_5	_5	5	_	40	_	
TC4053B										
		R <sub>L</sub> = 10 kΩ		-2.5	-2.5	2.5	_	0.15	_	
Total harmonic distortion	_	f = 1 kHz	(Note 2)	-5	-5	5	_	0.03	_	%
				-7.5	-7.5	7.5	_	0.02	_	
-50dB feedthrough	_	$R_L = 1 \text{ k}\Omega$ (Note	3)	-5	-5	5	<b>—</b> 50	0	_	kHz
(switch off)										
Crosstalk	_	$R_L = 1 \text{ k}\Omega$ (Note	4)	-5	-5	5	<u> </u>	5	_	MHz
Crosstalk		$R_{IN} = 1 k\Omega$		0	0	5	_	200	_	
(control-OUT)	_	$R_{OUT} = 10 \text{ k}\Omega$		0	0	10	_	400	_	mV
<u> </u>		C <sub>L</sub> = 15 pF		0	0	15	_	600	_	

Note 1: Sine wave of  $\pm 2.5 \ V_{p-p}$  shall be used for  $V_{is}$  and the frequency of 20 log 10  $\frac{V_{OS}}{V_{is}} = -3 \text{dB}$  shall be  $f_{max}$ .

Note 2:  $V_{is}$  shall be sine wave of  $\pm \left( \frac{V_{DD} - V_{EE}}{4} \right)$  p-p .

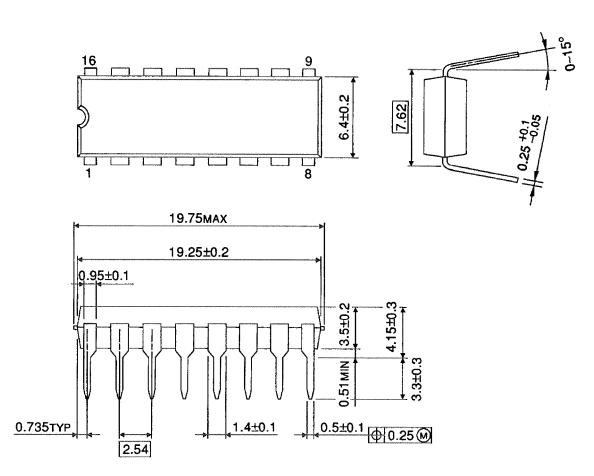
Note 3: Sine wave of  $\pm 2.5 \text{ V}_{\text{p-p}}$  shall be used for  $\text{V}_{\text{is}}$  and the frequency of 20 log 10  $\frac{\text{V}_{\text{OS}}}{\text{V}_{\text{is}}} = -50 \text{dB}$  shall be feed-through.

Note 4: Sine wave of  $\pm 2.5 \text{ V}_{\text{p-p}}$  shall be used for  $\text{V}_{\text{is}}$  and the frequency of 20 log 10  $\frac{\text{V}_{\text{OS}}}{\text{V}_{\text{is}}} = -50 \text{dB}$  shall be crosstalk.

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# **Package Dimensions**

DIP16-P-300-2.54A Unit: mm

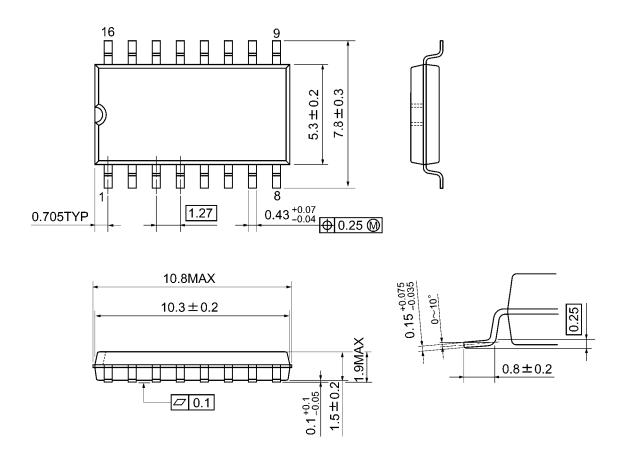


Weight: 1.00 g (typ.)



# **Package Dimensions**

SOP16-P-300-1.27A Unit: mm



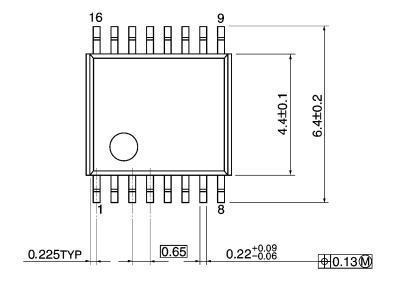
8

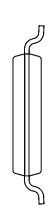
Weight: 0.18 g (typ.)

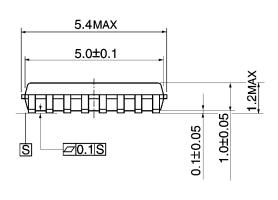
# **Package Dimensions**

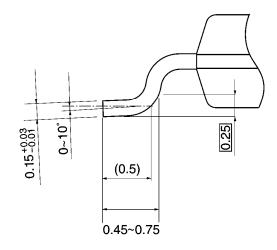
TSSOP16-P-0044-0.65A

Unit: mm









Weight: 0.06 g (typ.)

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