AN7171NK-LC, AN7173K-LC

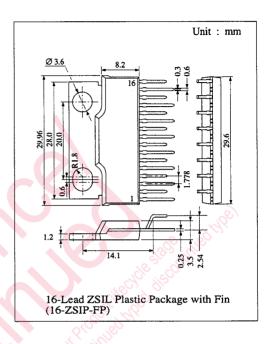
Dual BTL 14W Audio Power Amplifier

Description

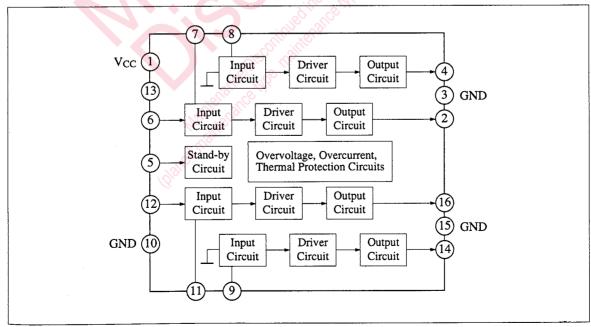
The AN7171NK-LC and AN7173K-LC are the monolithic integrated circuits designed for 14W (13.2V, 4Ω) output power amplifier. Stereo operation is enabled due to incorporating two BTL amplifiers in one chip. High reliability is obtained due to incorporating various protectors. ON/OFF is enabled even if power is supplied to power supply pin by incorporating stand-by circuit.

■ Features

- High output power (BTL 14W x 2)
- Incorporating stand-by circuits
- Incorporating protection circuits. (Temperature, overvoltage, V_{OUT}-GND short, Load short, V_{OUT}-V_{CC} short)
- Low shock noise from power ON/OFF operation
- Fewer external components
- Highly stable operation



Block Diagram



■ Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Rating	Unit
Supply Voltage	V _{CC} *1	24	V
Supply Current	I _{CC}	6 ,	A
Supply Voltage Surge	V _{CC(surge)} *2	50	V
Power Dissipation	P _D *3	62.5	W
Operating Ambient Temperature	Topr	-30 ~ +75	°C
Storage Temperature	Tstg	-55 ~ +150	°C

Operating Supply Voltage Range: $V_{CC} = 8.0V \sim 18.0V$

■ Electrical Characteristics ($V_{CC}=13.2V$, $R_L=4\Omega$, f=1kHz, $T_a=25^{\circ}C$)

Item	Symbol	Condition	min.	typ.	max.	Unit
Quiescent Current	I _{CQ}	$V_{in} = 0mV$		120	200	mA
Output Noise Voltage	V _{no} *	$V_{in} = 0$ mV, $R_g = 10$ k Ω		0.6	1.5	mV
Voltage Gain	Gv	$V_{in} = 5mV$	49.5	51.5	53.5	dΒ
Total Harmonic Distortion	THD	$V_{in} = 5mV$		0.2	0.75	%
Maximum Output Power	Po	THD = 10%	9.0	12.5	,	W
Ripple Rejection	RR*	$R_g = 0\Omega$, $V_{in} = 0$ mV, $V_r = 300$ mV, $f_r = 120$ Hz	35	40		dB
Output Offset Voltage	V _{O(offset)}	V _{in} = 0mV	-250	0	+250	mV
Channel Balance	СВ	$V_{in} = 5mV$	-1	0	+1	dB
Total Harmonic Distortion	THD	$V_{in} = 5 \text{mV}, f = 100 \text{Hz}$		0.26		%
Total Harmonic Distortion	THD	$V_{in} = 5 \text{mV}, f = 10 \text{kHz}$		0.45		%
Frequency Characteristics	f _{CH}	$V_{in} = 5 \text{mV}, -3 \text{dB down}$		22		kHz
Frequency Characteristics	f _{CL}	$V_{in} = 5 \text{mV}$, -3dB down		21		Hz
Quiescent Circuit Current at Stand-by Pin ON	I _{CQ(STBY-ON)}	Stand-by Pin ON		21		μА
Crosstalk	СТ	$V_{in} = 5mV$, $R_g = 10k\Omega$		61		dB

^{*} With 15Hz ~ 30kHz (12dB/OCT) filter

■ Pin

Pin No	Pin Name	Pin No	Pin Name	
1	Vcc	9	N.F.B. Ch.2	
2	Output Ch.1	10 >	GND (Input)	
3	GND (Output Ch.1)	11	N.F.B. Ch.2	
4	Output Ch.1	12	Input Ch.2	
5	Stand-by	13	Ripple Filter	
6	Input Ch.1	14	Output Ch.2	
7	N.F.B. Ch.1	15	GND (Output Ch.2)	
8	N.F.B. Ch.1	16	Output Ch.2	

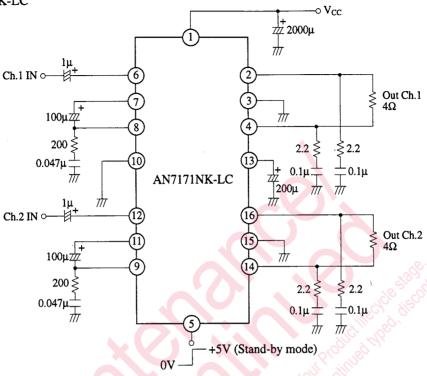
^{*1} No-signal

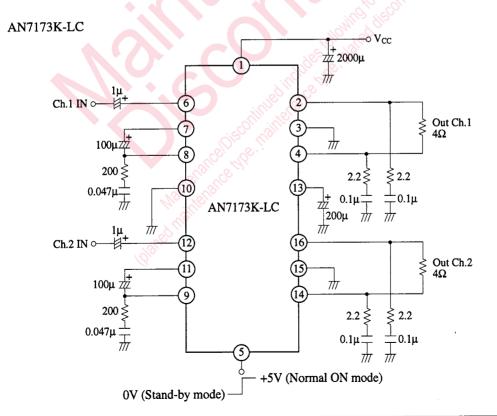
^{*2} Time = 0.2s

^{*3} $R\theta_{j-c} = 2^{\circ}C/W$

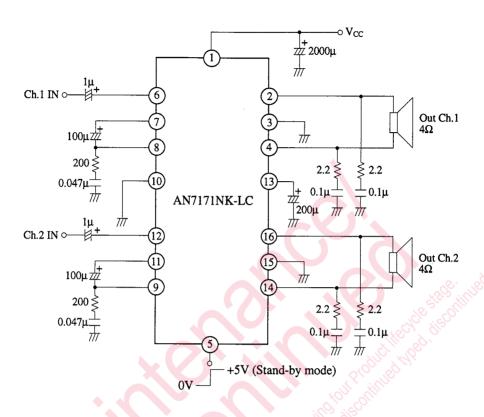
Test Circuit



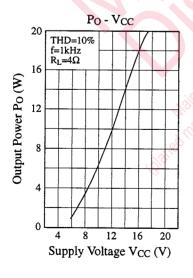


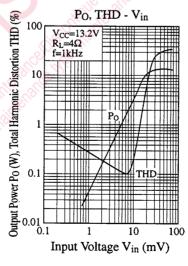


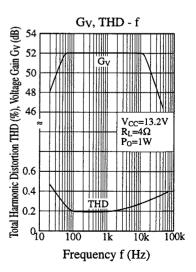
■ Application Circuit



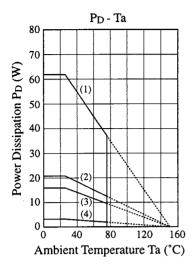
■ Characteristics Curve





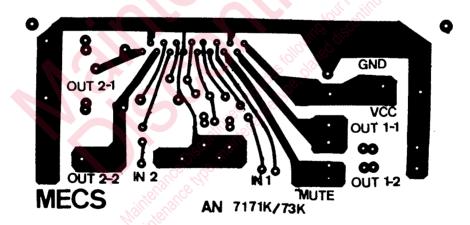


■ Characteristics Curve (Continue)



- (1) $Tc=Ta,62.5W (\theta_{i-c}=2^{\circ}C/W)$
- (2) 20.8W (θ_f=4°C/W) With a 100cm² x 3mm Al heat sink (black colour coated) or a 200cm² x 2mm Al heat sink (not lacquered)
- (3) 15.6W (θ_f=6°C/W) With a 100cm² x 2mm Al heat sink (not lacquered)
- (4) 3.0W at Ta=30°C Without heat sink

■ Printed Circuit Board Layout (Scale: 1:1)



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