LB11867FV

ON Semiconductor®

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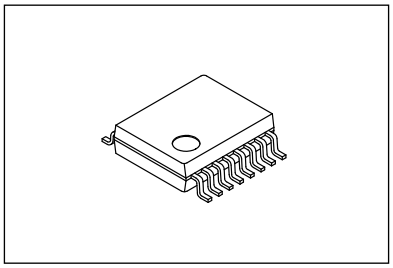
Monolithic Digital IC

For Fan Motor
Variable Speed Single-phase Full-wave Pre-driver

Overview

LB11867FV is a single-phase bipolar driving motor pre-driver with the variable speed function compatible with external PWM signal. With a few external parts, a highly-efficient and highly-silent variable drive fan motor with low power consumption can be achieved. This product is best suited for driving of the server requiring large air flow and large current and the fan motor of consumer appliances.



SSOP16 (225mil)

Feature

- Single-phase full-wave driving pre-driver
  ⇒ Low-saturation drive using external PMOS-NMOS enables high-efficiency low power-consumption drive.
- Variable speed control possible with external PWM input
  ⇒ Separately-excited upper direct PWM (f =30kHz) control method ensures highly silent speed control.
- Current limiting circuit incorporated
  ⇒ Chopper type current limiting made at startup and during lock.
- Reactive current cut circuit incorporated
  ⇒ Reactive current before phase changeover is cut, ensuring highly silent and low power-consumption drive.
- Minimum speed setting pin
  ⇒ Minimum speed can be set by setting the resistance.
- Soft start setting pin
- Lock protection and automatic reset circuits incorporated
- FG (rotation speed detection) output
- Thermal shutdown circuit incorporated

Typical Applications

- Computing & Peripherals
- Industrial
- Server
- Vending Machine

ORDERING INFORMATION

See detailed ordering and shipping information on page 11 of this data sheet.

June 2015 - Rev. 1

Publication Order Number :
LB11867FV/D

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
V_{CC} pin maximum supply voltage	V_{CC} max		18	V
OUTN pin maximum output current	IOUTN max		20	mA
OUTP pin maximum Sink current	IOUTP max		20	mA
OUT pin output withstand voltage	VOUT max		18	V
VTH, RMI pins withstand voltage	VVTH, VRMI max		7	V
S-S pin withstand voltage	V_{S-S} max		7	V
FG output pin withstand voltage	V_{FG} max		19	V
FG pin maximum output current	I_{FG} max		10	mA
5VREG pin maximum output current	I5VREG max		20	mA
Allowable power dissipation	Pd max	with specified substrate *1	800	mW
Operating temperature	Topr	*2	-30 to 95	°C
Storage temperature	Tstg		-55 to 150	°C

*1 Specified substrate: 114.3mm×76.1mm×1.6mm, glass epoxy board.

*2 Ti max=150°C must not be exceeded.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Recommended Operating Conditions at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
V_{CC} Supply voltage	V_{CC}		5.5 to 16	V
VTH, RMI input voltage range	VTH, RMI		0 to 5	V
Hall input common-phase input voltage range	VICM		0.2 to 3	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

Electrical Characteristics at Ta = 25°C, V_{CC} = 12V

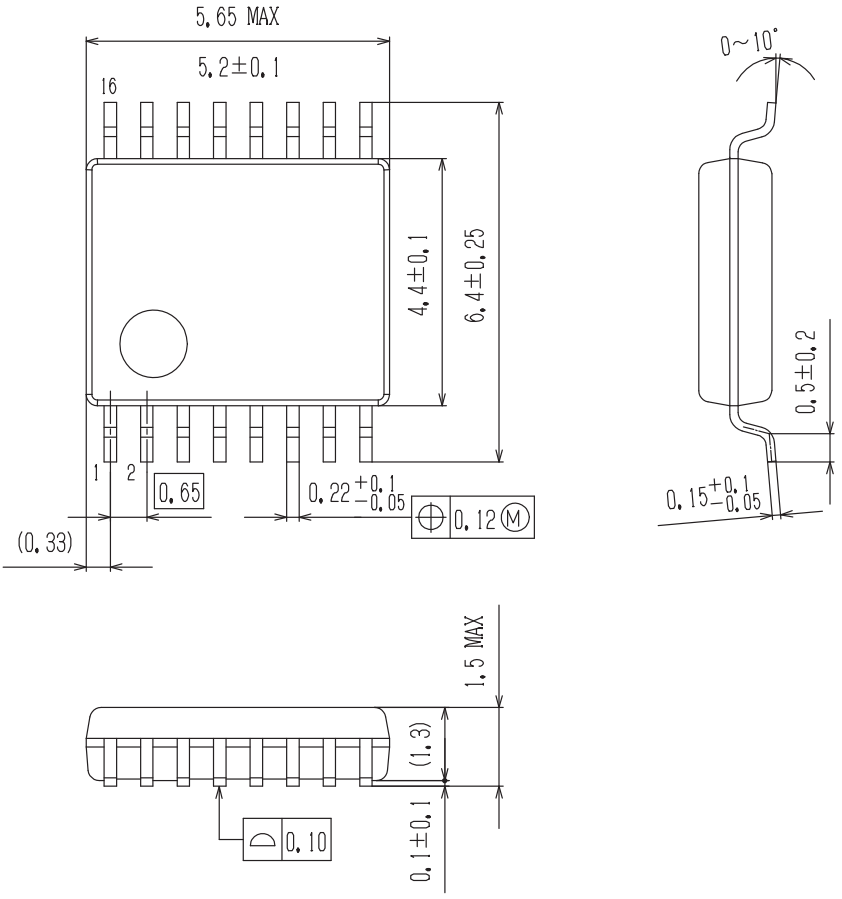
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Circuit current	$I_{CC}1$	During drive	5.5	7.5	9.5	mA
	$I_{CC}2$	During lock protection	5.5	7.5	9.5	mA
5VREG voltage	5VREG	I5VREG = 5mA	4.80	4.95	5.10	V
Current limiting voltage	VLIM		185	200	215	mV
CPWM pin “H” level voltage	$V_{CPWM}H$		2.8	3.0	3.2	V
CPWM pin “L” level voltage	$V_{CPWM}L$		0.9	1.1	1.3	V
CPWM pin charge current	$I_{CPWM}1$	V_{CPWM} = 0.5V	24	30	36	μA
CPWM pin discharge current	$I_{CPWM}2$	V_{CPWM} = 3.5V	21	27	33	μA
CPWM Oscillation frequency	FPWM	C = 220PF		30		kHz
CT pin “H” level voltage	$V_{CT}H$		2.8	3.0	3.2	V
CT pin “L” level voltage	$V_{CT}L$		0.9	1.1	1.3	V
CT pin charge current	$I_{CT}1$	V_{CT} = 0.5V	1.6	2.0	2.5	μA
CT pin discharge current	$I_{CT}2$	V_{CT} = 3.5V	0.16	0.20	0.25	μA
CT pin charge/discharge ratio	R_{CT}	$I_{CT}1/I_{CT}2$	8	10	12	times
S-S pin discharge current	I_{S-S}	V_{S-S} = 1V	0.4	0.5	0.6	μA
OUTN output H-level voltage	V_ONH	I_O = 10mA		V_{CC}-0.85	V_{CC}-1.00	V
OUTN output L-level voltage	V_ONL	I_O = 10mA		0.9	1.00	V
OUTP output L-level voltage	V_OPL	I_O = 10mA		0.5	0.65	V
Hall input sensitivity	VHN	IN^+, IN^- differential voltage (including offset and hysteresis)		±10	±20	mV
FG output L-level voltage	$V_{FG}L$	I_{FG} = 5mA		0.15	0.30	V
FG pin leakage current	$I_{FG}L$	V_{FG} = 19V			20	μA
VTH/RMI pin bias current	IVTH/IRMI	CPWM = VTH/RMI = 2V			0.1	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

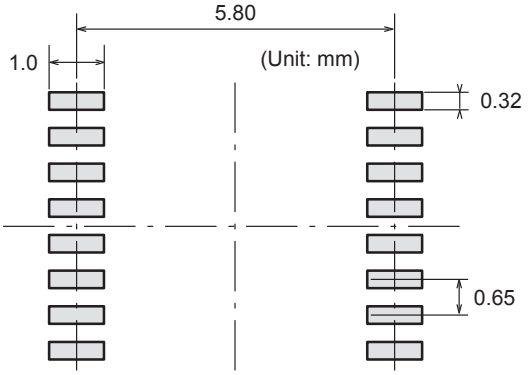
Package Dimensions

unit : mm

SSOP16 (225mil)
CASE 565AM
ISSUE A



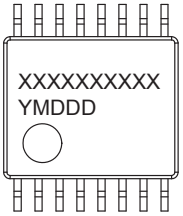
SOLDERING FOOTPRINT*



NOTE: The measurements are not to guarantee but for reference only.

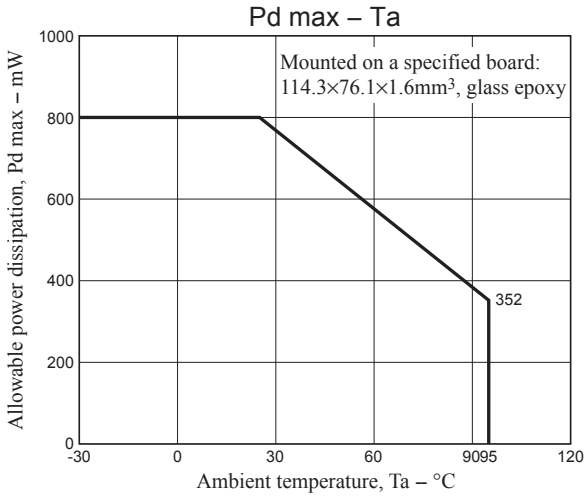
*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*

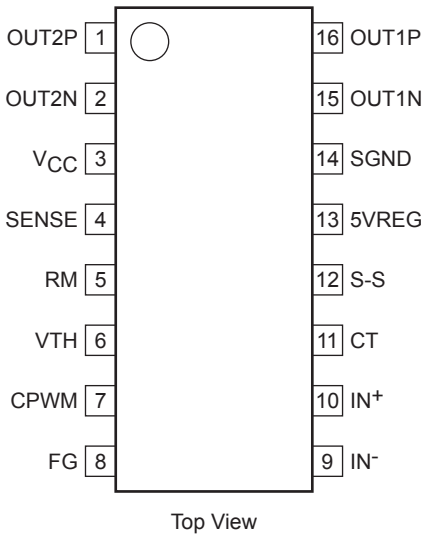


XXXXX = Specific Device Code
Y = Year
M = Month
DDD = Additional Traceability Data

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “ ▪”, may or may not be present.

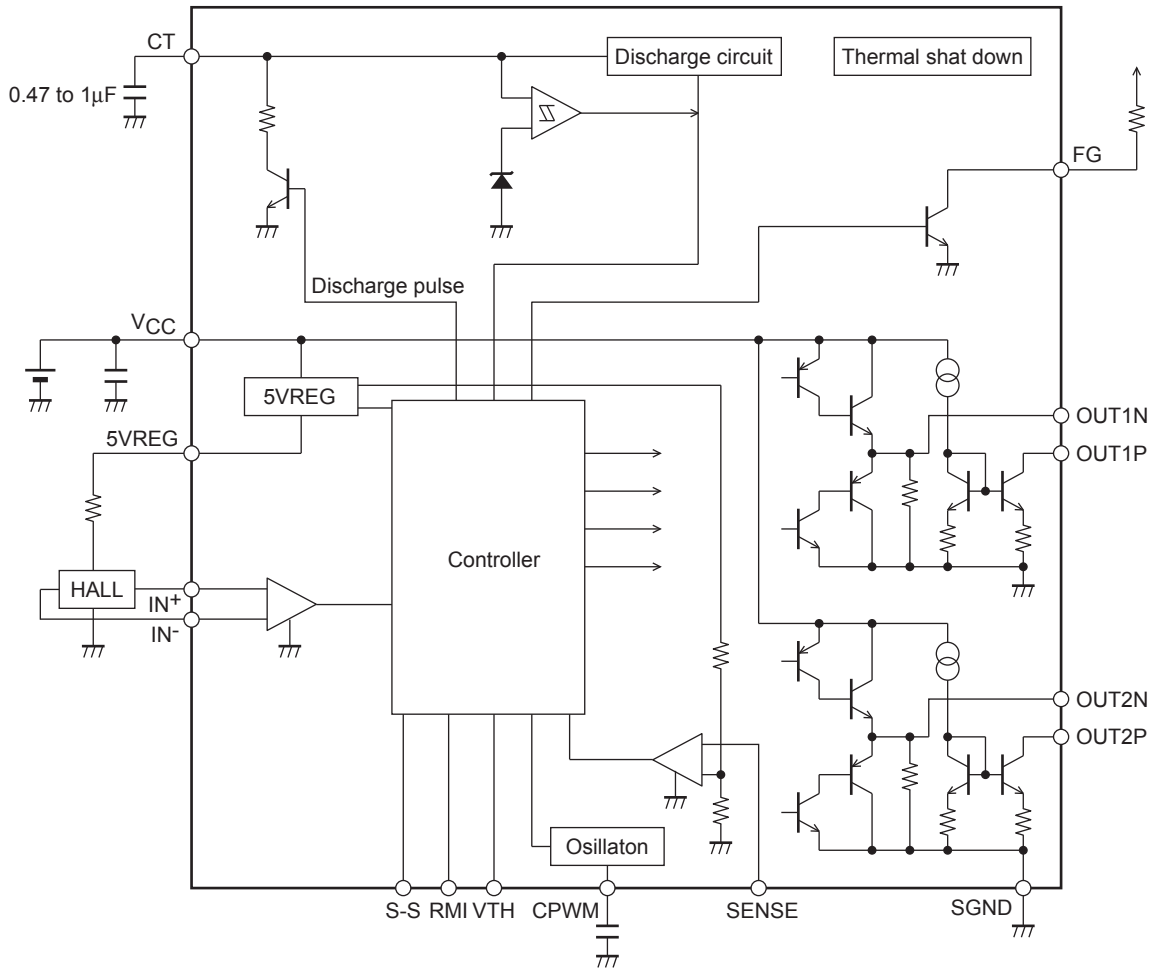


Pin Assignment



Top View

Block Diagram



Truth table

(1) Drive lock CPWM=H VTH, RMI, S-S=L

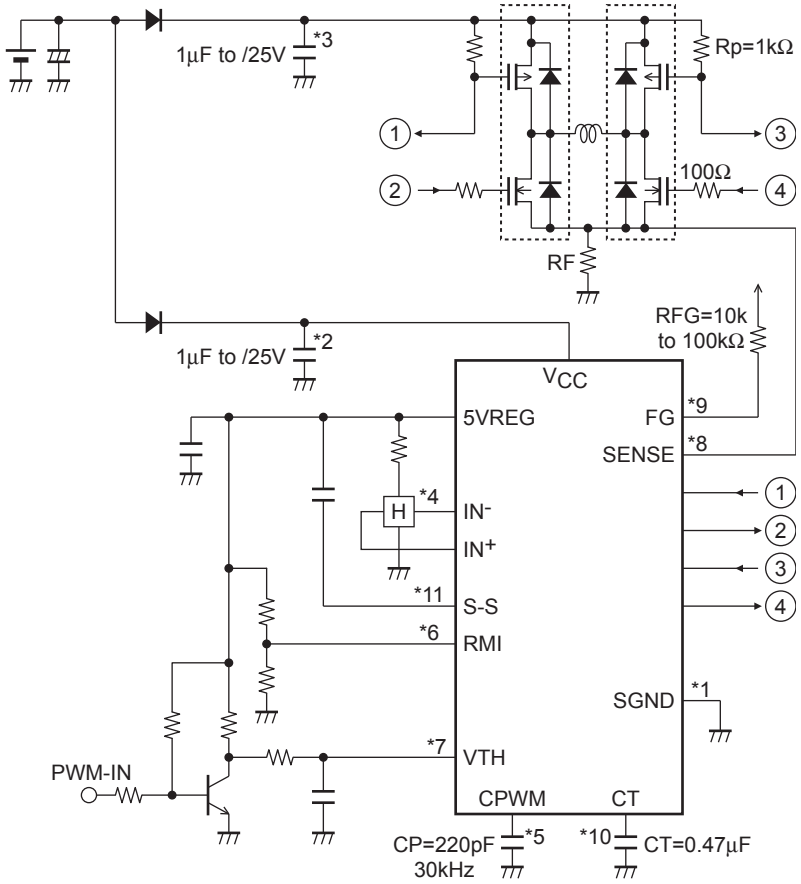
IN-	IN+	CT	OUT1P	OUT1N	OUT2P	OUT2N	FG	Mode
H	L	L	L	L	OFF	H	L	OUT1 → 2 drive
L	H		OFF	H	L	L	OFF	OUT2 → 1 drive
H	L	H	OFF	L	OFF	H	L	Lock protection
L	H		OFF	H	OFF	L	OFF	

(2) Speed control CT, S-S=L

VTH, RMI	CPWM	IN-	IN+	OUT1P	OUT1N	OUT2P	OUT2N	Mode
L	H	H	L	L	L	OFF	H	OUT1 → 2 drive
		L	H	OFF	H	L	L	OUT2 → 1 drive
H	L	H	L	OFF	L	OFF	H	Regeneration mode
		L	H	OFF	H	OFF	L	

For VTH, RMI, and S-S pins, refer to the timing chart.

Application Circuit Example (12V)



*1. Power–GND wiring
SGND is connected to the control circuit power supply system.

*2. Power stabilization capacitor
For the power stabilization capacitor on the signal side, use the capacitance of 1μF or more.
Connect V_{CC} and GND with a thick and shortest pattern.

*3. Power stabilization capacitor on the power side
For the power stabilization capacitor on the power side, use the capacitance of 1μF or more.
Connect the power supply on the power side and GND with a thick and shortest pattern.

*4. IN^+, IN^- pins
Hall signal input pin.
Wiring should be short to prevent carrying of noise.
If noise is carried, insert the capacitor between IN^+ and IN^- pins.
The Hall input circuit functions as a comparator with hysteresis (15mV).
This also has a soft switch section with ±30mV (input signal differential voltage).
It is also recommended that the Hall input level is minimum 100mV(p-p).

*5. CPWM pin
Pin to connect the capacitor for generation of the PWM basic frequency
The use of CP = 220pF causes oscillation at f = 30kHz, which is the basic frequency of PWM.
As this is used also for the current limiting canceling signal, be sure to connect the capacitor even when the speed control is not made.

*6. RMI pin

Minimum speed setting pin.
Perform pull-up with 5VREG when this pin is not to be used.
If the IC power supply is likely to be turned OFF first when the pin is used with external power supply, be sure to insert the current limiting resistor to prevent inflow of large current. (The same applies to the VTH pin.)

*7. VTH pin

Speed control pin.
Connect this pin to GND when it is not used (at full speed).
For the control method, refer to the timing chart.
For control with pulse input, insert the current limiting resistor and use the pin with the frequency of 20k to 100kHz (20kHz to 50kHz recommended).

*8. SENSE pin

Current limiting detection pin.
When the pin voltage exceeds 0.2V, the current is limited and the operation enters the lower regeneration mode.
Connect this pin to GND when it is not to be used.

*9. FG pin

Rotation speed detection pin.
This is an open collector output, which can detect the rotation speed from the FG output according to the phase changeover. Keep this pin open when it is not to be used.

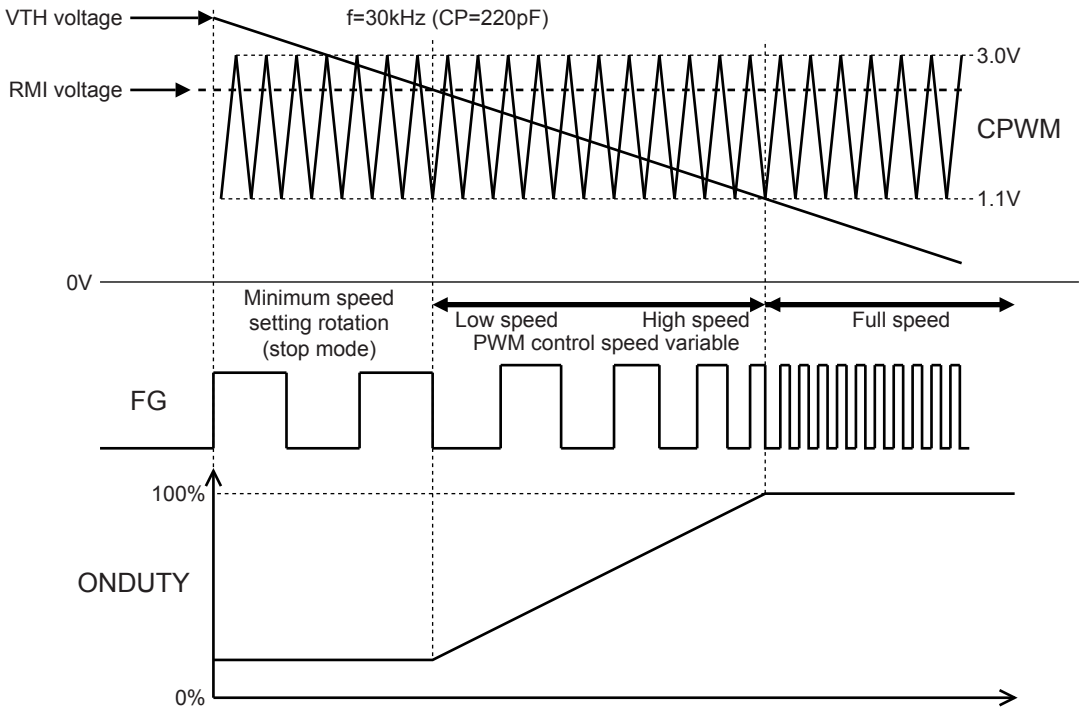
*10. CT pin

Pin to connect the lock detection capacitor.
The constant-current charge and discharge circuits incorporated cause locking when the pin voltage becomes 3.0V and unlocking when it is 1.1V.
Connect the pin to GND when it is not to be used (locking not necessary).

*11. S-S pin

Pin to connect the soft-start setting capacitor.
Connect the capacitor between 5VREG and S-S pin.
This pin enables setting of the soft start time according to the capacity of the capacitor.
See the timing char.
Connect the pin to GND when it is not to be used.

Control timing chart (Speed control)



(1) Minimum speed setting (stop) mode

The low-speed fan rotation occurs at the minimum speed set with the RMI pin. When the minimum speed is not set (RMI pin pulled up to 5VREG), the motor stops.

(2) Low speed⇔high speed

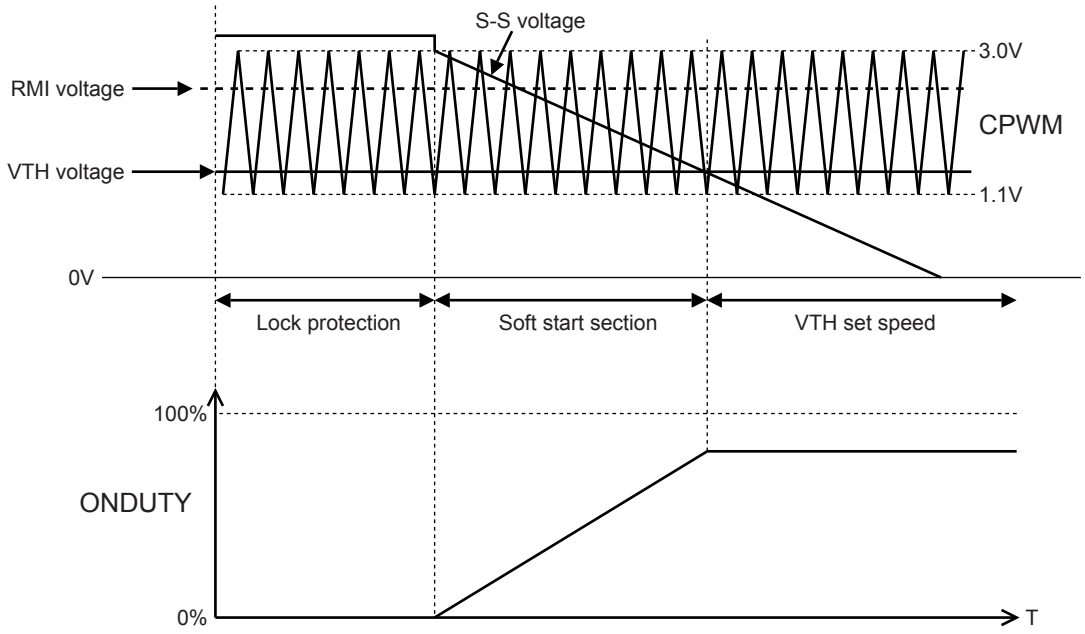
PMW control is made by comparing the CPWM oscillation voltage (1.1V⇔3.0V) and VTH voltage.
Both upper and lower output TRs are turned ON when the VTH voltage is low. The upper output TR is turned OFF when the VTH voltage is high, regenerating the coil current in the lower TR. Therefore, as the VTH voltage decreases, the output ON-DUTY increases, causing increase in the coil current, raising the motor rotation speed.
The rotation speed can be monitored with the FG output.

(3) Full speed mode

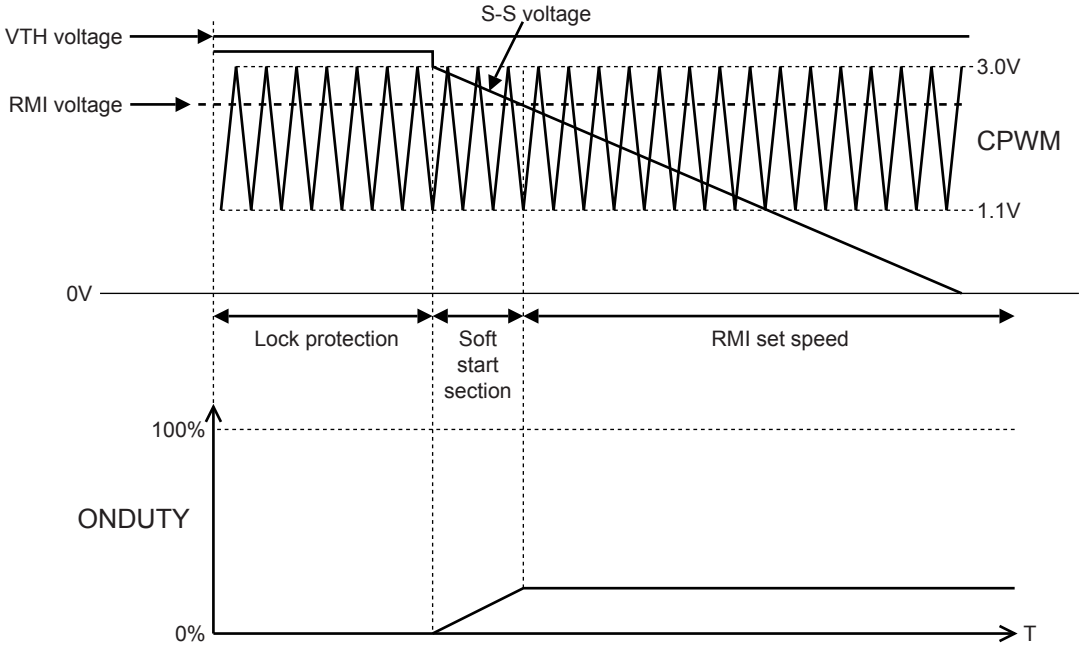
The full speed mode becomes effective when the VTH voltage is 1.1V or less. (Set VTH = GND when the speed control is not to be made.)

Control timing chart (Soft start)

(1)At VTH < RMI voltage



(2) At VTH > RMI voltage



Adjust the S-S pin voltage gradient by means of the capacitance of the capacitor between the S-S pin and 5VREG.
Recommended capacitor: 0.1μ to 1μF

ORDERING INFORMATION

Device	Package	Shipping (Qty / Packing)
LB11867FV-MPB-E	SSOP16 (225mil) (Pb-Free)	90 / Fan-Fold
LB11867FV-MPB-H	SSOP16 (225mil) (Pb-Free / Halogen Free)	90 / Fan-Fold
LB11867FV-TLM-E	SSOP16 (225mil) (Pb-Free)	2000 / Tape & Reel
LB11867FV-TLM-H	SSOP16 (225mil) (Pb-Free / Halogen Free)	2000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF