FY XDS100V3

Product Description:

The XDS110 is a new debug probe (emulator) for TI embedded processors. The XDS110 replaces the XDS100 series while supporting a wider range of standards (IEEE1149.1, IEEE1149.7, SWD) in a single pod. In addition, all XDS debug probes support core and system trace in all ARM and DSP processors that have an Embedded Trace Buffer (ETB).

The XDS110 connects to the target board via a TI 20-pin connector (with multiple adapters for TI 14-pin, and ARM 20-pin), and to the host PC via USB2.0 High Speed (480Mbps). It also provides two additional connections: an auxiliary 14-pin port connector that supports Energy Trace™, and a full-duplex UART port.

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Auxiliary 14-pin port connector with Energy Trace™ support, a full-duplex UART port.

Supports USB2.0 high speed interface with theoretical speed up to 480M/s. Simulation and debugging via

14P/20P interface.
Support Code Composer Studio (CCS) V7/V8/V9/V10 and higher versions or IAR.
It can be used in Win7/WIN8/WIN10 and other operating systems.

Features.

●USB2.0 high-speed interface, up to 480Mbit/s
●Dual output level shifter chip (double buffered)
●USB side self-recovery insurance protection, to protect your computer
●JTAG side electrostatic chip protection, anti-static 15KV, stable and reliable
●Supports power failure detection
●Supports adaptive clock
●Supports JTAG reset and wait for reset boot mode.
●Supports power-on reset boot mode
●Supports multiple FTDU device drivers.
●Support adjustable TCLK clock.
●Supports 1.8V~5V JTAG voltage standard.
●Support TI standard 14PIN JTAG interface.
●Support TI standard 20PIN JTAG interface.
●Support 2-wire cJTAG interface, simplify the connection with the target board driver-free, plug ●in the emulator automatically recognized.
●Support high-speed USB code download
●Supports DSP FLASH in-circuit emulation.
●Faster emulation and download speeds
●Support CCS V7 or above, the new version of CCSV10 are supported.
●Support IAR for ARM V7.5 or above.
●Support Flash Programmer 2 burning software
●Support uniflash standalone programming software.
●Support SmartRF Studio configuration software.
●Support Win7/Win8/Win10 and other operating systems.

New Features:

Serial Wire Debug (SWD) and Serial Wire Output (SWO) available for selected microcontrollers and wireless connectivity microcontrollers

Core and system trace is available through ETB on selected ARM and DSP processors

For more information on the tracking capabilities of a specific device, refer to its Technical Reference Manual (TRM).

Supported Chip List.

Supported chips list:

SimpleLink MCUs (CC13xx, CC26xx, CC3x, MSP432)

C2000, TM4C12x and Hercules microcontrollers

Sitara (AM335x, AM43xx, AM57xx, AM65xx, AMIC1xx)

Automotive SoCs (TDAx ADAS, DRAx infotainment)

mmWave sensors (IWR/AWR14xx, IWR/AWR16xx, IWR68xx)

C674x and C66x (Keystone I) Floating point DSPs

C642x and C645x

66AK2 and TCI66x Multicore DSP + ARM® SoCs (Keystone II)

C55x Low-power DSPs

UCD3x Digital Power devices

PGA970 SoC

Other TI SoCs with PRU, C674x, C66xx, Cortex M, Cortex R and Cortex A cores

Supported Software:

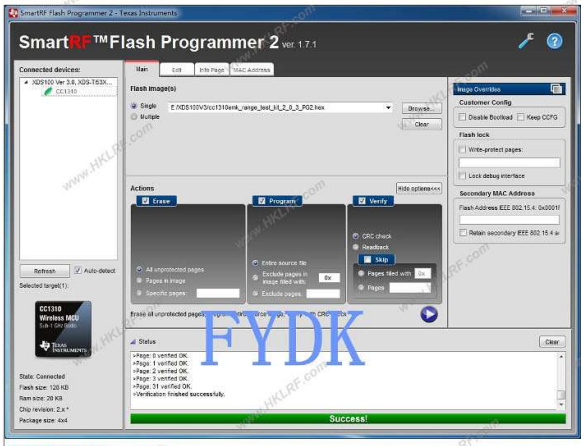
Installation of Code Composer Studio v7 (or later) or a compatible third-party development environment.

Support CCS7 and above, or IAR7.5

Shipping List.

1, XDS110 emulator 1 set
2, USB2.0 high-speed connection line 1
3, the import of 14/20P JTAG color line 1
5, the network disk connection, containing software / related instructions / DSP development board information.
Note: does not support the following software CCS7.

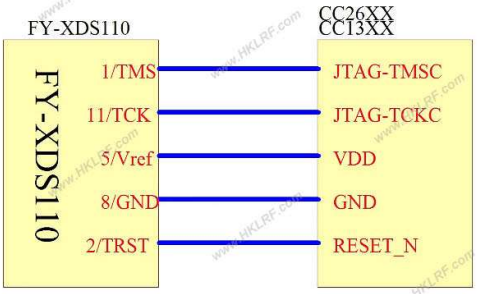
CC1310 burn-in.



CC2640 burn-in.

How to wire.

C2JTAG Wiring.



4-wire JTAG wiring method.