Cadence Virtuoso Studio IC23.10.060 / SPECTRE 23.10.242

Description

Cadence's products, such as Cadence IC Design, enable creativity and innovation in electronic design globally and play a key role in the development of today's integrated electronic circuits. Customers use Cadence's services, IP addresses, hardware and software to design and approve sophisticated computer systems and communications equipment, energy and semiconductors. The company's headquarters include headquarters in California, St. Joe's, and research centers around the world to serve the global electronics industry.

Cadence IC Design software includes tools for designing fully integrated circuits including: pattern input, behavioral modeling (Verilog-AMS), orbital simulation, custom molding, physical verification and verification, extraction and interpretation (notes) Background.

Cadence IC Design is primarily used for cell-standard designs, RF, synthetic signal and analog, but is also used in memory and FPGA designs. Optional automation (automation) of non-critical aspects of custom IC designs allows engineers to focus on the industrial decisions of their designs.

Cadence Circuit Design Solutions provide fast and easy access to design concepts, including design goal management in a way that flows naturally in the schematics. Using this advanced, noise-aware environment, you can simulate many of the intrinsic dependencies of analog, RF, and synthetic signal designs and determine their impact on circuit efficiency.

Cadence IC Design Virtuoso Series Components:

– **Virtuoso Schematic Editor (art)**

Provides a fully customizable environment and limit combinations for start-to-finish, digital-to-consumer, RF, and signal designs.

– **Virtuoso Analog Design Environment**

Provides a perceptual set of capabilities for statistical and electrical analysis, verification and optimization of signal-hybrid / analog designs that include interfaces for many industry-standard simulators.

– **Virtuoso template package for electrically aware design**

The unique feature of the Electric Design Internal Review, the Cadence Virtuoso layout package with the Electric Design Aware (EAD) feature, enhances the design team's versatility and circuit efficiency for custom ICs.

– **Virtuoso analysis and virtualization**

Virtualization and Analytics in Cadence Virtuoso is a waveform analysis and visualization tool that efficiently and effectively analyzes the performance of hybrid, RF, and analogue signal designs.

Cadence IC Design Virtuoso Software Package Tips:

– It was not possible to install and prepare the activation guide and its method is shown in the text files in the Crack folder.

– MMSIM software that performs the simulation task is presented separately.

– To run this software in Windows you can use Linux installed in VMware .

required system

Supported Architectures: x86
System Requirements: Linux
Supported Operating Systems: RHEL 5, RHEL 6, SLES 11.0

Pictures


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Virtuoso Studio
Custom design for the real world
Virtuoso Studio sets the new industry standard for doing analog, mixed-signal, RF and photonics design.Customers get access to the same Virtuoso experience they are accustomed to with improvements that address the latest design challenges.
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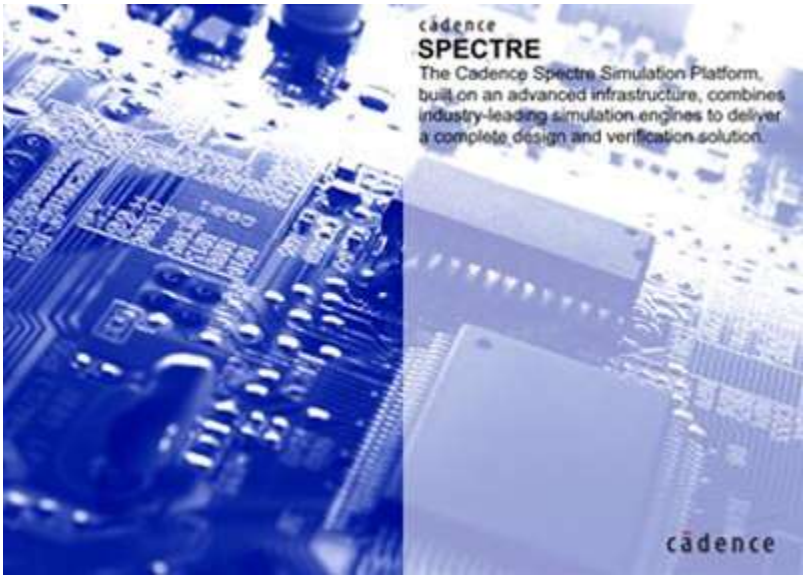
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SPECTRE
The Cadence Spectre Simulation Platform, built on an advanced infrastructure, combines industry-leading simulation engines to deliver a complete design and verification solution.
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